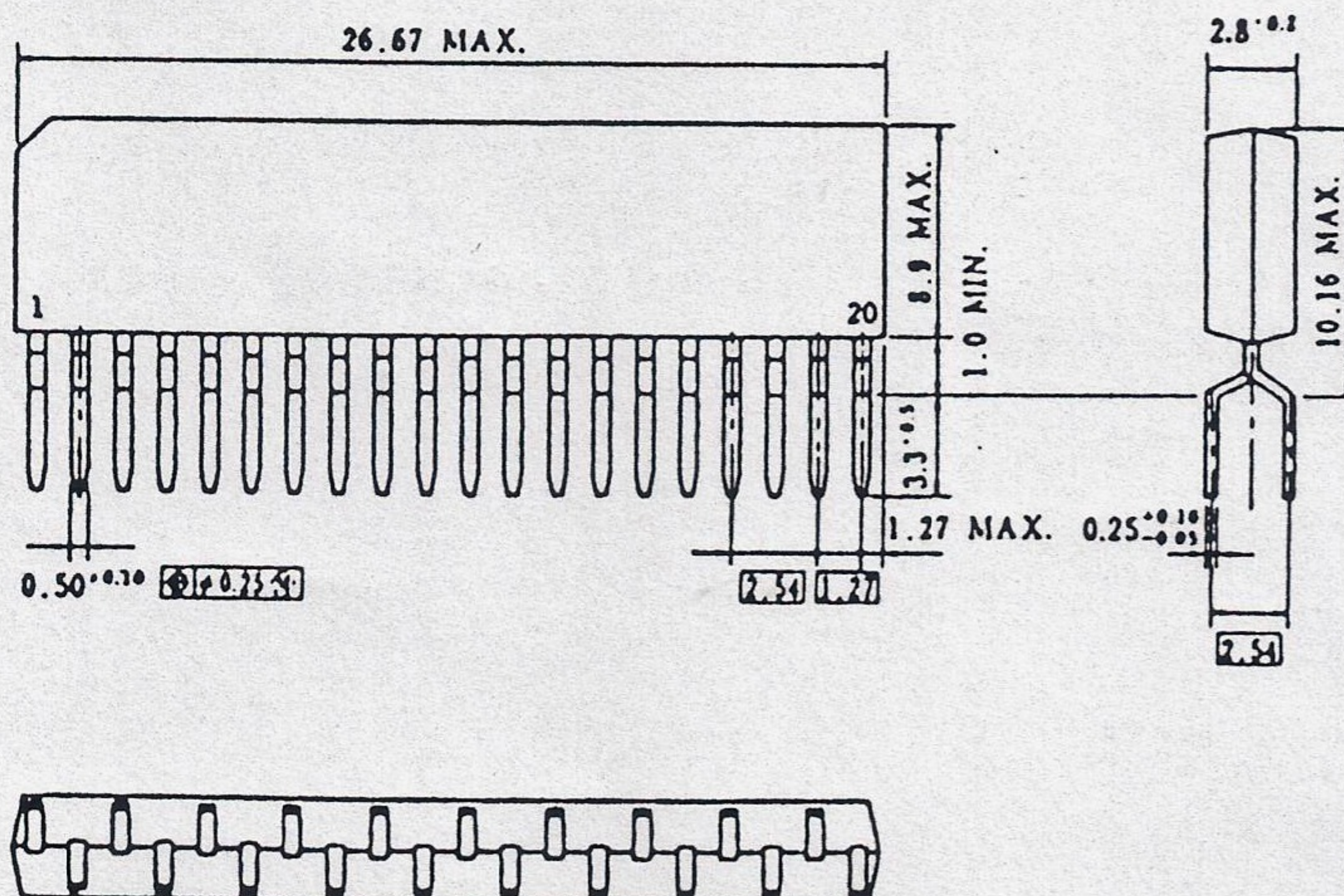


PIN ASSIGNMENTS

A ₀	12	33	CAS
D _{OUT}	13	34	CND
D _{IN}	14	35	WE
RAS	15	36	NC
NC	16	37	NC
A ₆	17	38	A ₁
A ₅	18	39	A ₂
V _{CC}	19	40	A ₃
A ₄	20	41	A ₄
A ₇	21	42	A ₅

PACKAGING CONFIGURATION



ATARI CORPORATION

SIZE
A

DRAWING NO.

C103641-xxx

REV

A

SCALE NONE

SHEET 2 OF 2

THIRD ANGLE SYSTEM DIMENSION mm


REV	REVISIONS DESCRIPTION	DATE	APPROVED
A	PRODUCTION RELEASE	10-24-89	<i>[Signature]</i>

IC DRAM CMOS 1M x 1 ZIP

PART NO.	APPROVED VENDOR	VENDOR PART NO.	ACCESS TIME
C103641-001	NEC	μ PD421000V-12	120 nsec
	TOSHIBA	TC511000Z-12	
	FUJITSU	MB81C1000-12	
C103641-002	NEC	μ PD421000V-10	100 nsec
	TOSHIBA	TC511000Z-10	
	FUJITSU	MB81C1000-10	
C103641-003	NEC	μ PD421000V-85	80 nsec
	TOSHIBA	TC511000Z-85	85 nsec
	FUJITSU	MB81C1000-85	

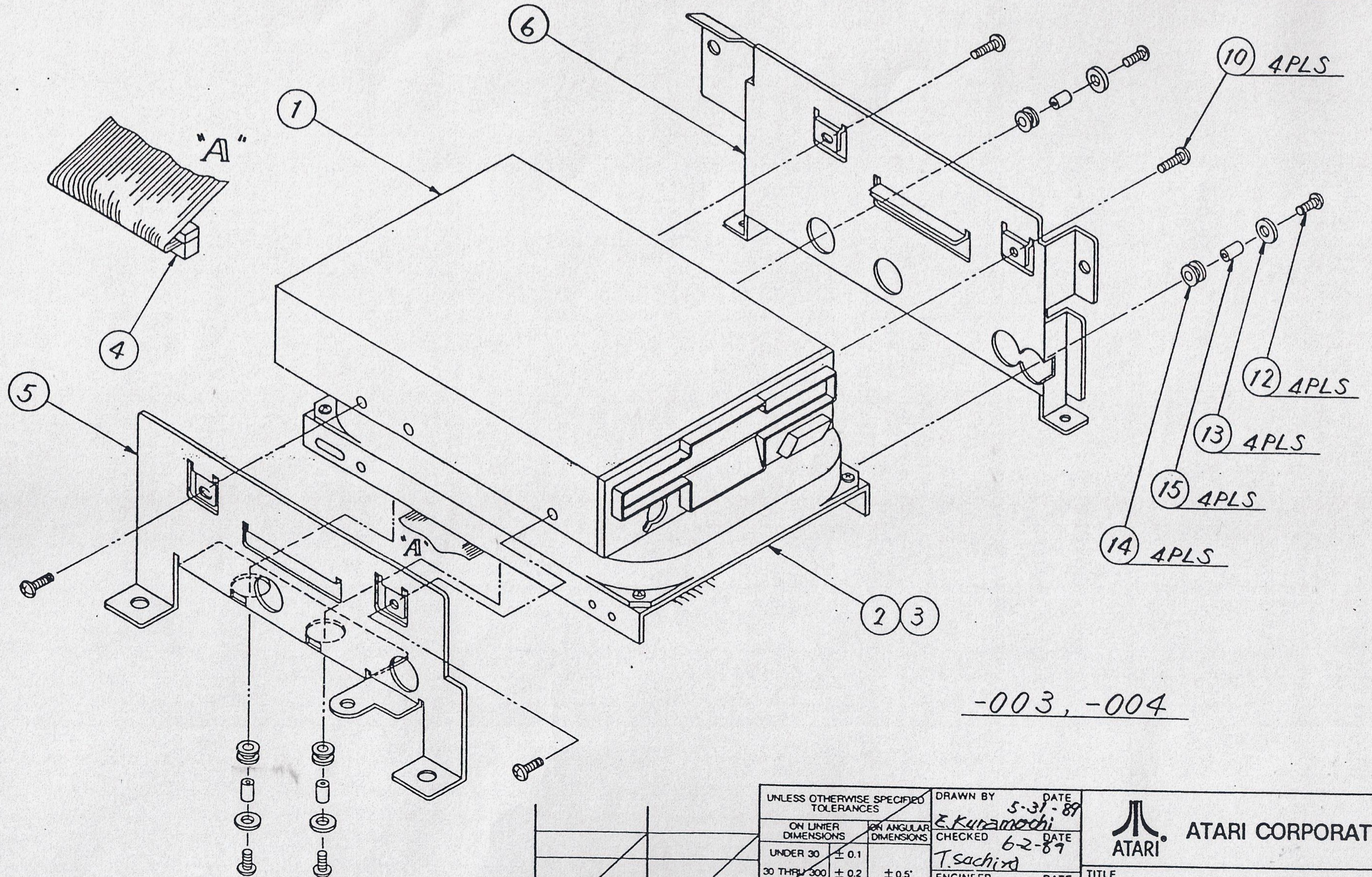
1. THIS DRAWING SHOWS PIN ASSIGNMENT AND PACKAGING CONFIGURATION FOR ZIP PACKAGE ONLY.
REFER TO DWG NO. C101712-XXX (DIP PACKAGE) FOR OTHER SPECIFICATIONS.


NOTES - UNLESS OTHERWISE SPECIFIED

<div>CA200527-xxx</div> <div>CA200528-xxx</div> <div>NEXT ASSY</div> <div>USED ON ORIGINAL MODEL</div> <div>APPLICATION</div>		<div>DRAWN BY</div> <div>E. Kuramochi</div> <div>CHECKED</div> <div>T. Tsuchiya</div> <div>ENGINEER</div> <div>A. Shinagawa</div> <div>APPROVED</div> <div>S. OKAHARA</div>	<div>DATE</div> <div>6-2-89</div> <div>DATE</div> <div>6-3-89</div> <div>DATE</div> <div>6-3-89</div> <div>DATE</div> <div>6/3/89</div>	<div>  <div>ATARI CORPORATION</div> </div> <div>TITLE</div> <div>IC DRAM CMOS 1M x 1 ZIP</div>			
		<div>DATE</div> <div>6-3-89</div>		<div>SIZE</div> <div>A</div>		<div>DRAWING NO.</div> <div>C103641-xxx</div>	<div>REV</div> <div>A</div>
		<div>DATE</div> <div>6/3/89</div>		<div>SCALE</div> <div>NONE</div>		<div>SHEET</div> <div>1</div> <div>OF</div> <div>2</div>	

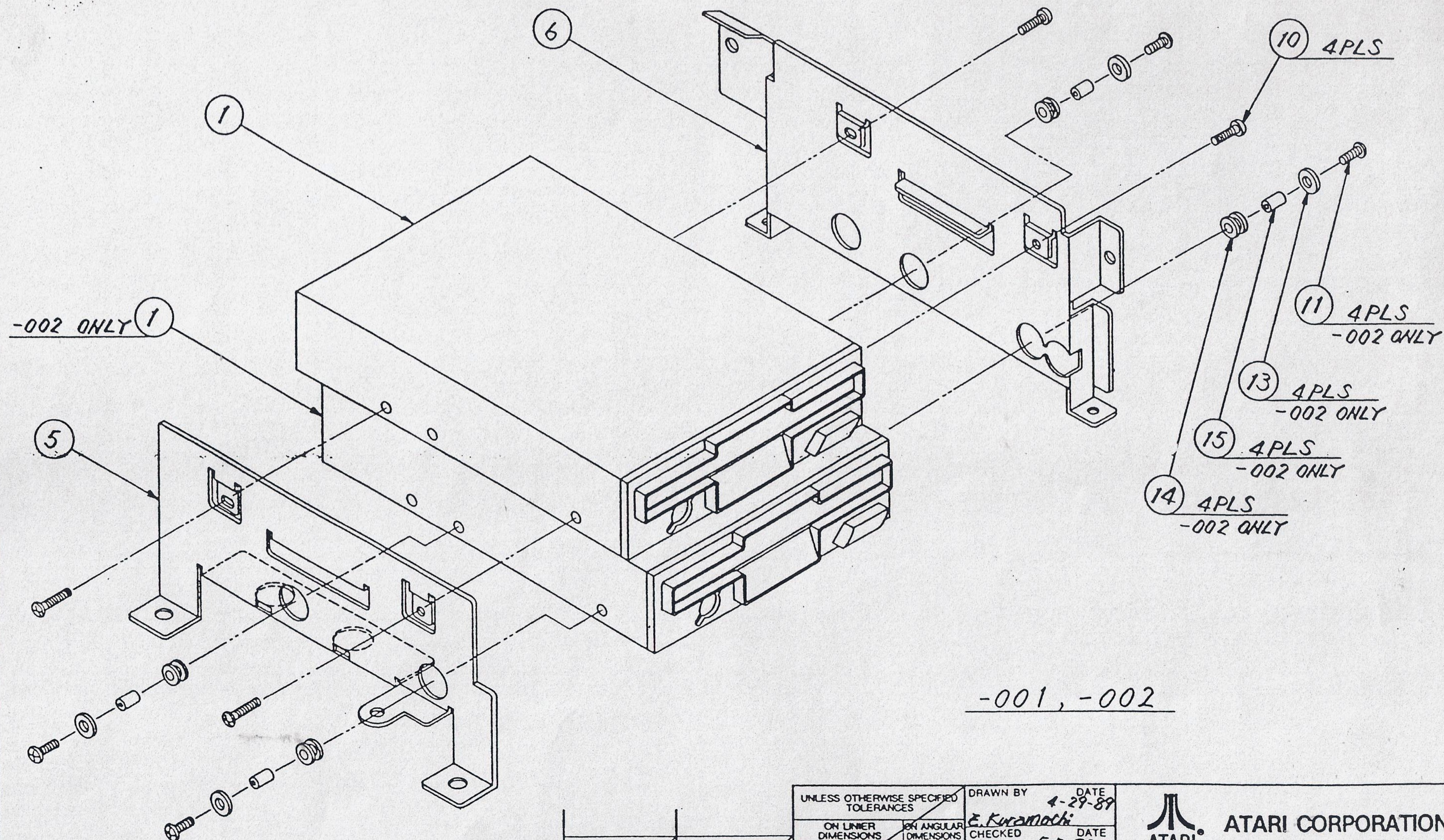
THIRD ANGLE SYSTEM. DIMENSION : mm

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



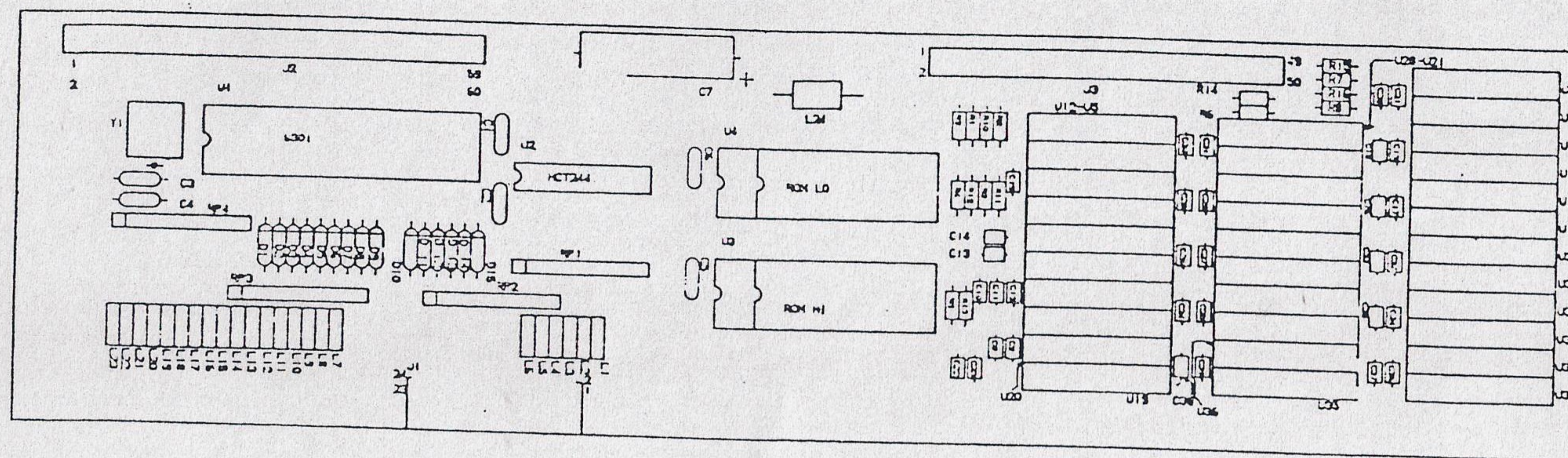
UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY	DATE	 ATARI CORPORATION	
ON LINEAR DIMENSIONS	ON ANGULAR DIMENSIONS	<i>E. Kuramochi</i>	5-31-89		
UNDER 30	± 0.1	CHECKED	DATE	TITLE ASSY DRIVE LST	
30 THRU 300	± 0.2	<i>T. Sachid</i>	6-2-89		
OVER 300	± 0.4	ENGINEER	DATE	SIZE DRAWING NO. REV	
MATERIAL		<i>mm</i>	6-3-89		
USED ON		APPROVED	DATE	B CA200470-xxx B	
		<i>ills</i>	6/3/89		

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		




UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY <i>E. Kuramachi</i> DATE <i>4-29-89</i>		ATARI CORPORATION	
ON LINER DIMENSIONS		CHECKED <i>T. Tsuchiya</i> DATE <i>5-3-89</i>			
UNDER 30	± 0.1	ON ANGULAR DIMENSIONS	ENGINEER <i>W. L. Smith</i> DATE	TITLE <i>ASSY DRIVE LST</i>	
30 THRU 300	± 0.2	± 0.5°		SIZE <i>R</i>	DRAWING NO. <i>CA2000A70-XXX</i>
OVER 300	± 0.4		APPROVED <i>W. L. Smith</i> DATE	REV <i>D</i>	

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



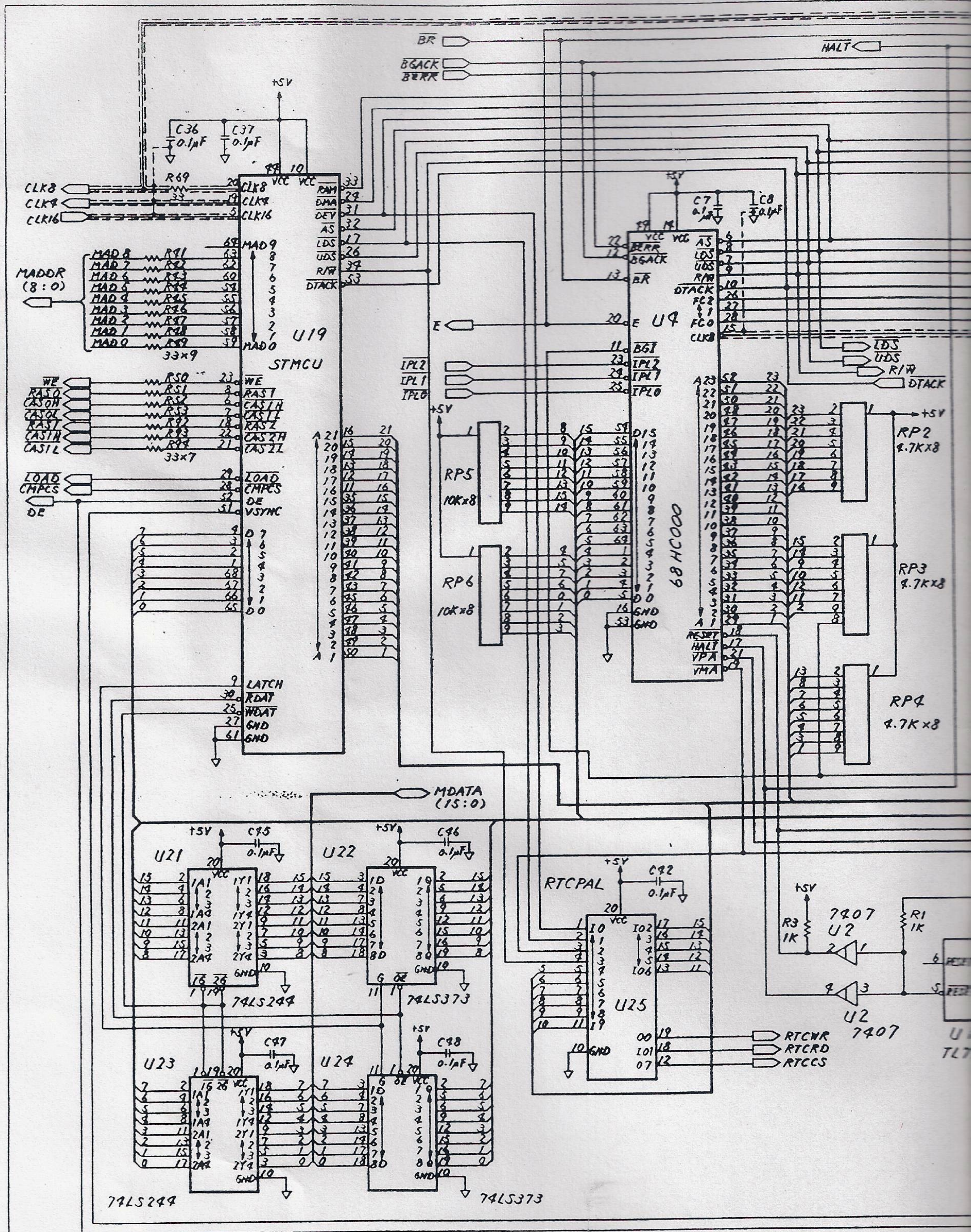
NOTES—UNLESS OTHERWISE SPECIFIED:

UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY		DATE		 ATARI CORPORATION	
		ON LINEAR DIMENSIONS		ON ANGULAR DIMENSIONS			
UNDER 30		±		±		TITLE	
30 THRU 300		±		±		ASSY SUB PCB LST-4	
OVER 300		±		±		DRAWING NO.	
MATERIAL		FINISH		APPROVED		SCALE	
NEXT ASSY		USED ON ORIGINAL MODEL		DATE		REV	
APPLICATION		DATE		DATE		SHEET 6 OF 6	

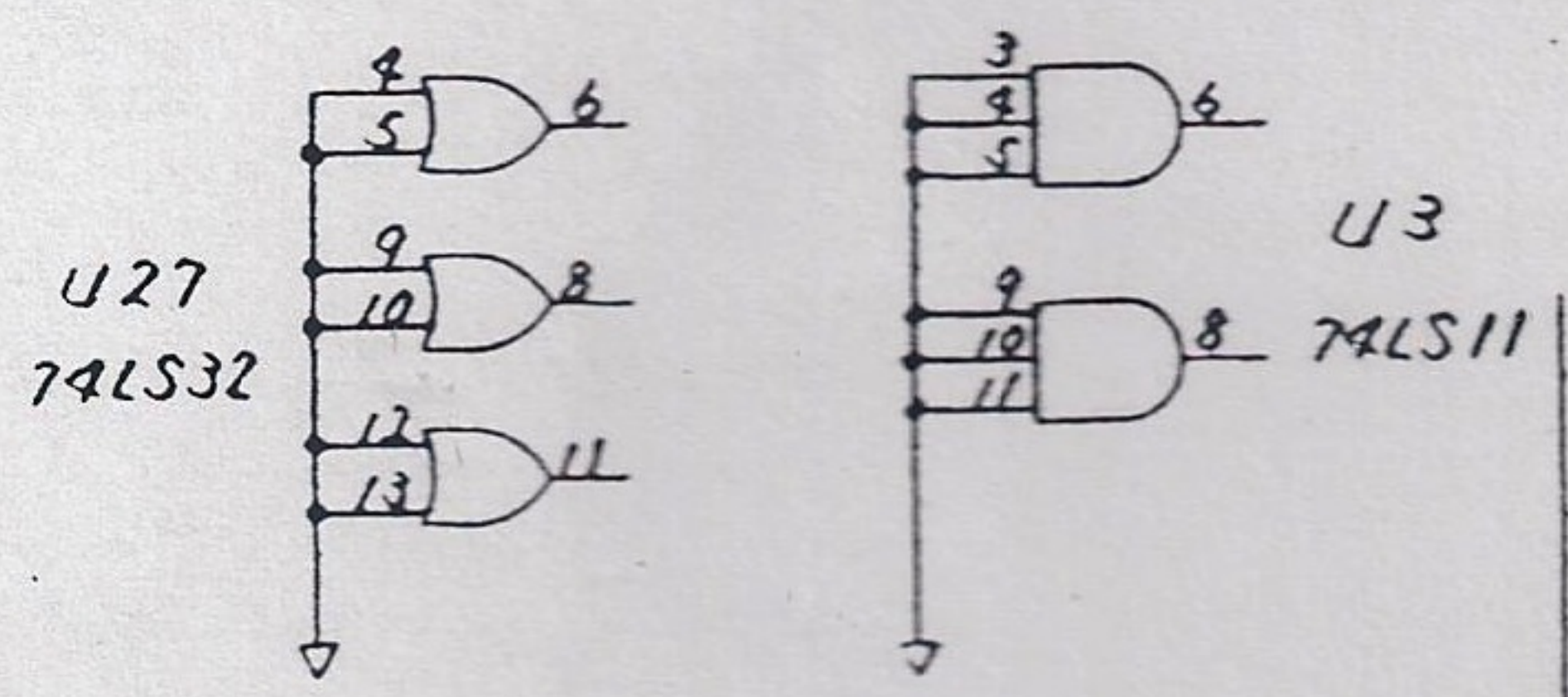
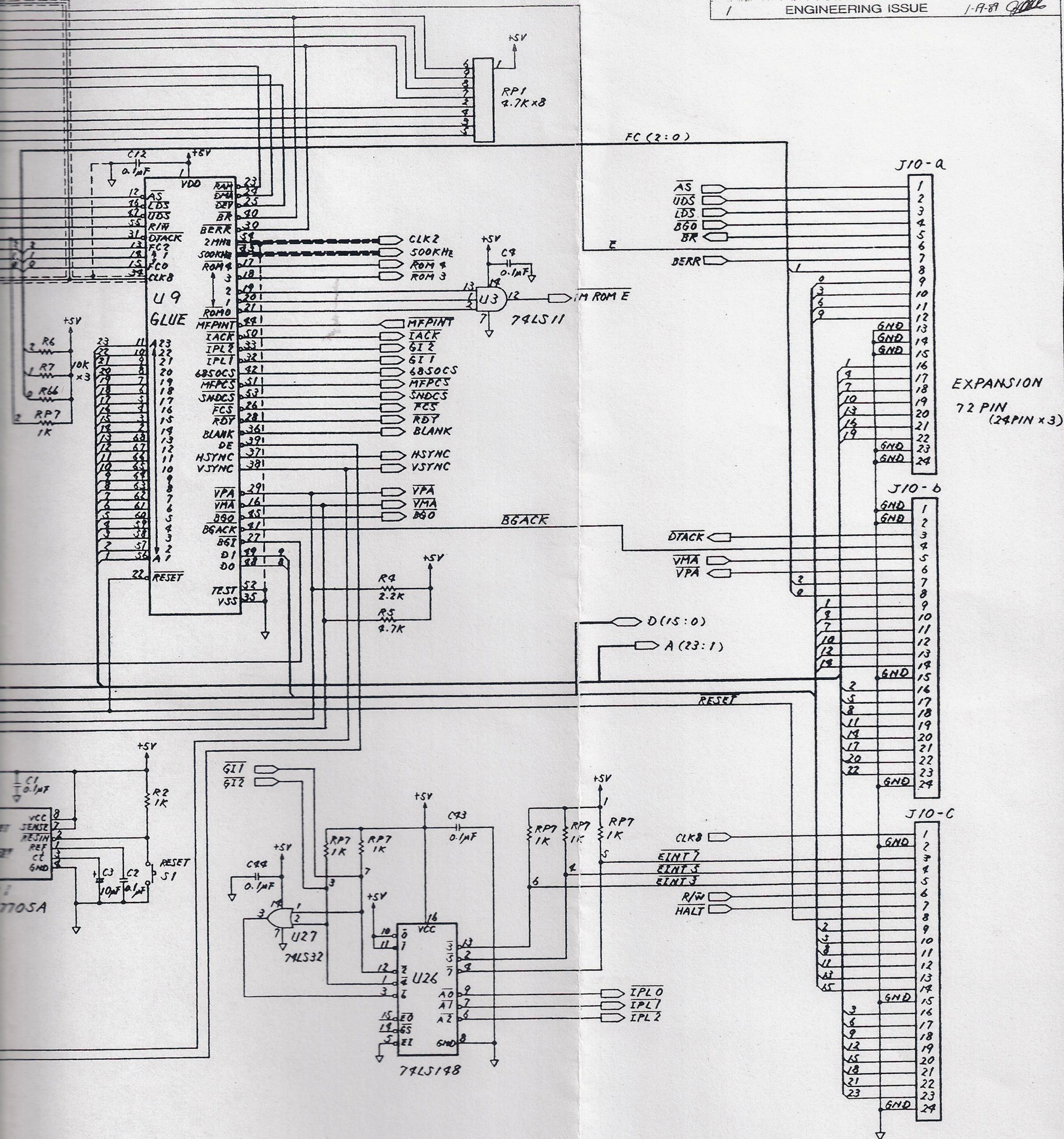
CHECKED: *L. L. L.* 6-2-89
 ENGINEER: *T. Tschida* 6-2-89
 APPROVED: *A. Shinagawa* 6-2-89
 APPROVED: *J. L. L.* 6/3/89
 APPROVED: *S. OKADA* 6/3/89

SIZE: **B**
 DRAWING NO.: **CA 200.528-xxx**
 SCALE: **NONE**
 SHEET: **6 OF 6**

THRU 300 & SYSTEM DIMENSIONS: 1/16"

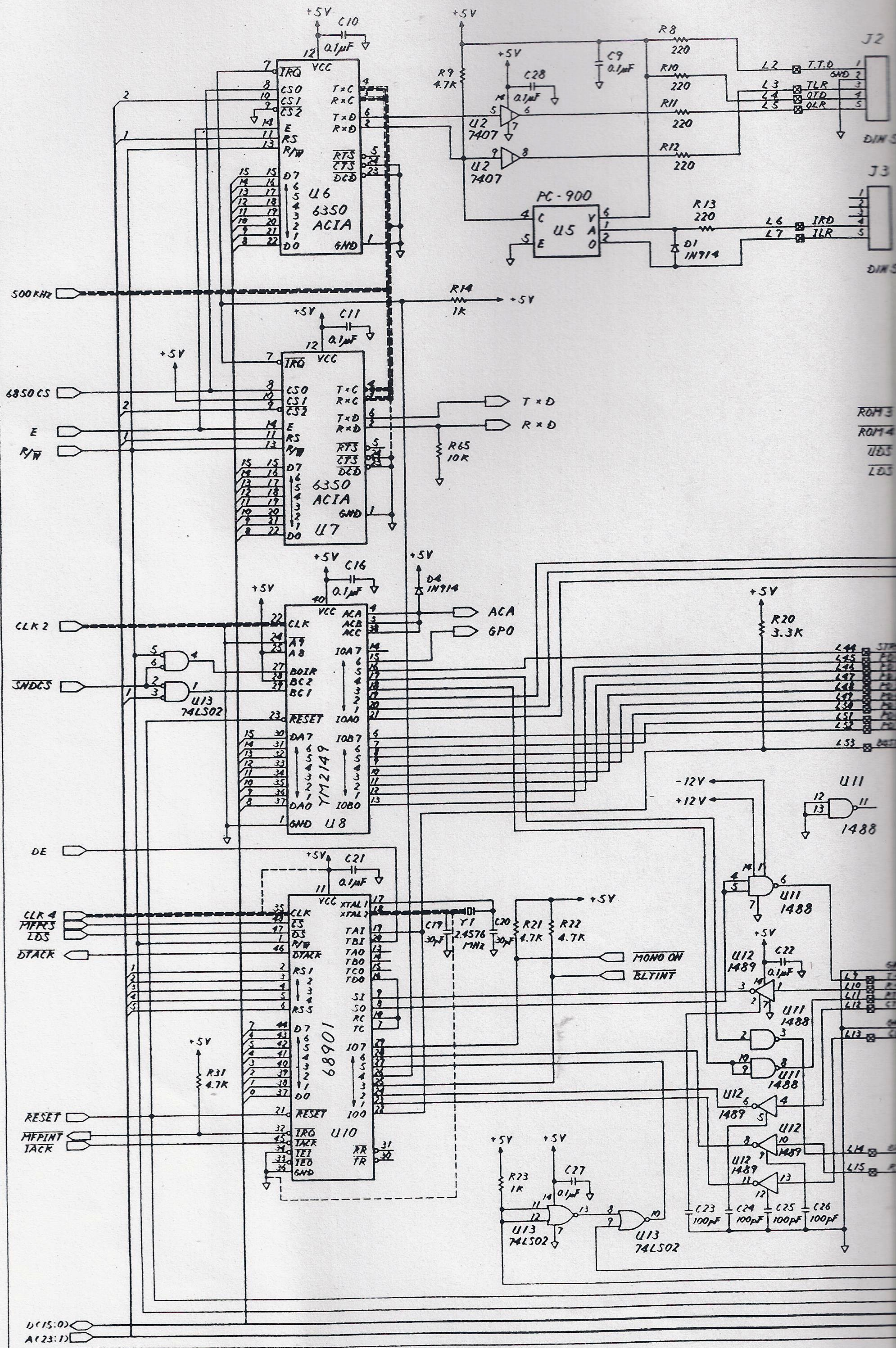


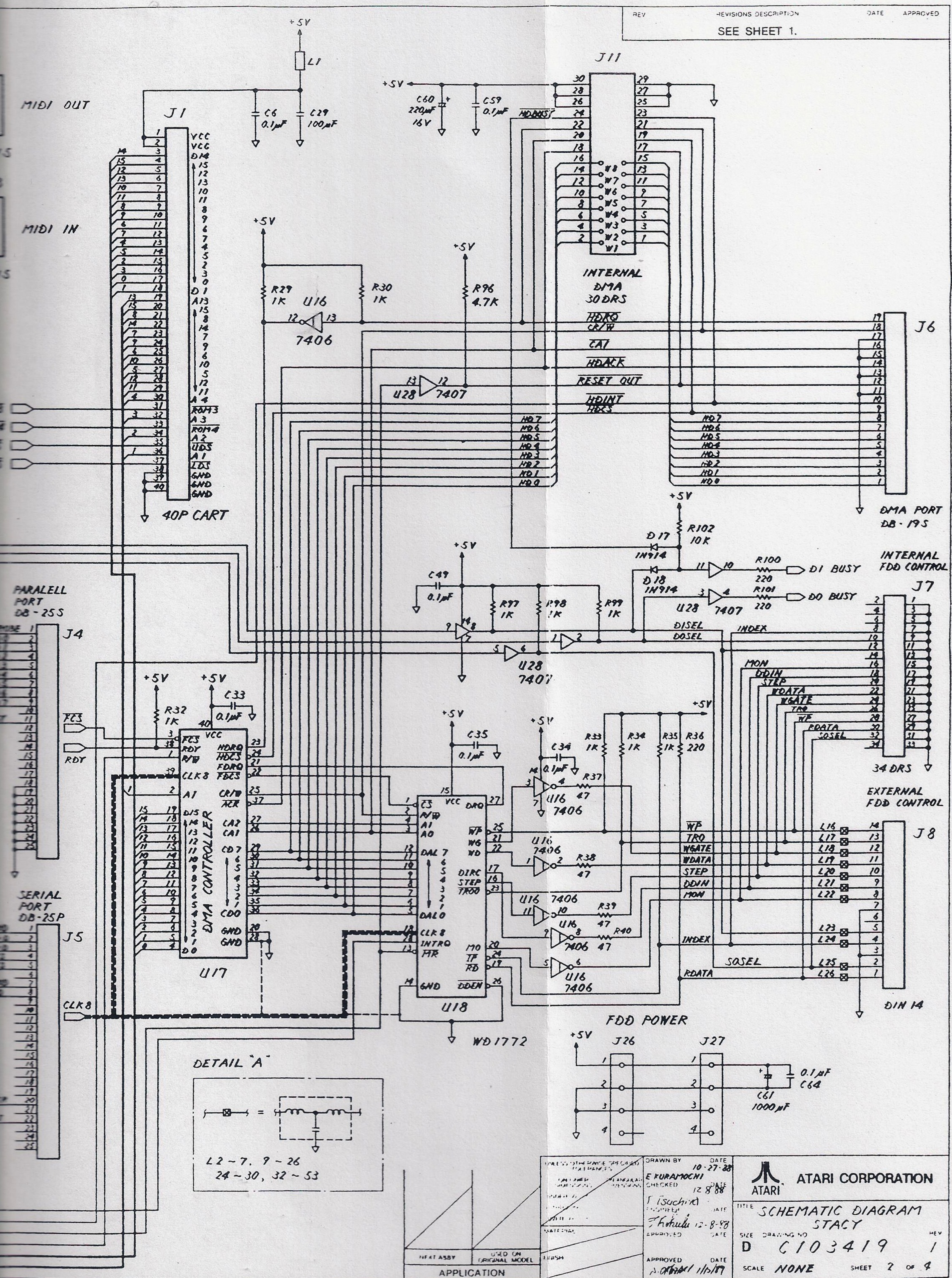
1 REFERENCE: PCB BATTERY CONNECTOR (P/N C103498).
 PCB FABRICATION BATTERY CONNECTOR (P/N C103497).
 2 NOISE FILTER: SEE DETAIL "A".
 1 RESISTORS ARE MEASURED IN OHMS.
 NOTES: UNLESS OTHERWISE SPECIFIED:

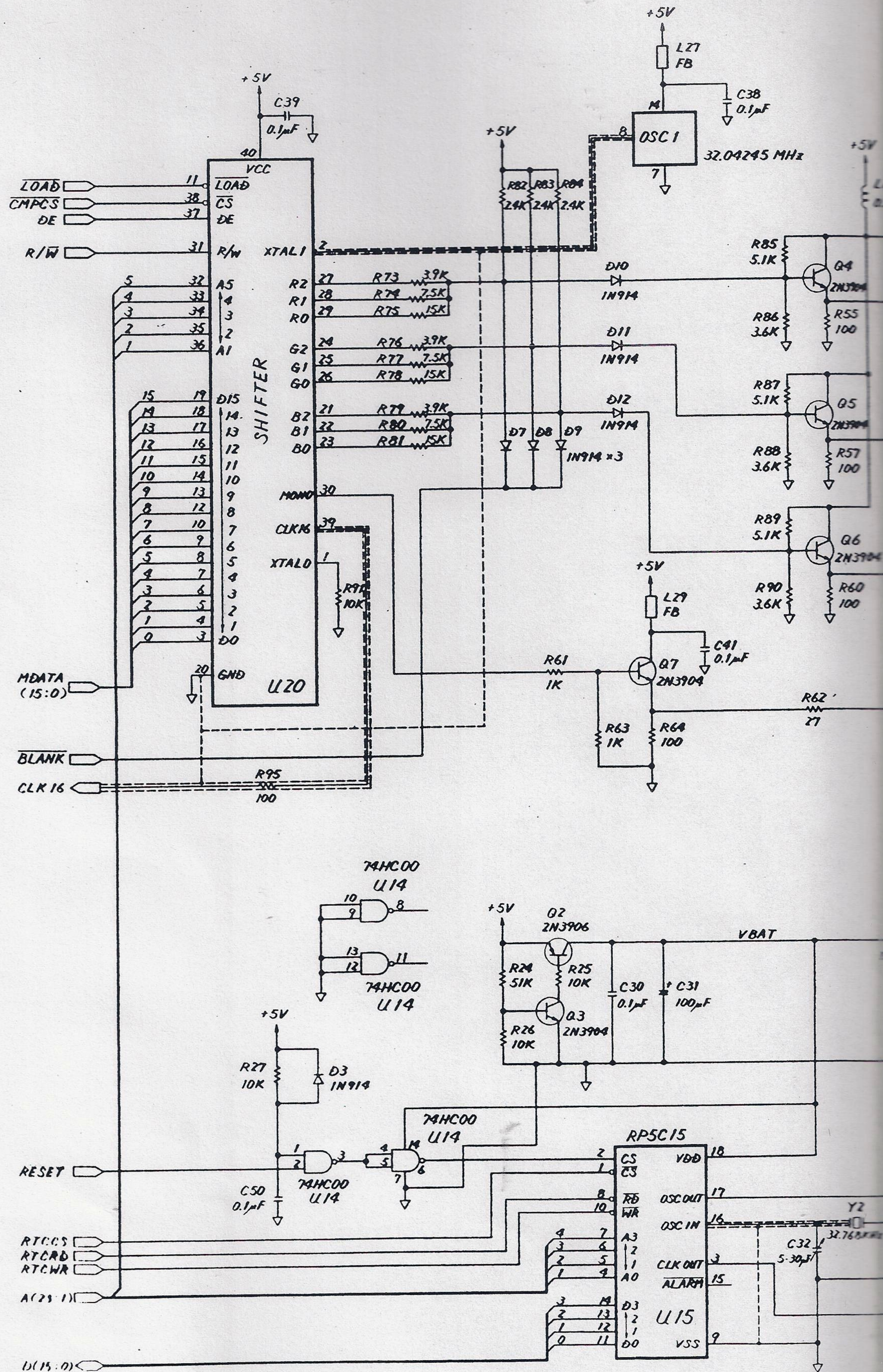


CA200469-XXX STACY
NEXT ASSY USED ON ORIGINAL MODEL
APPLICATION

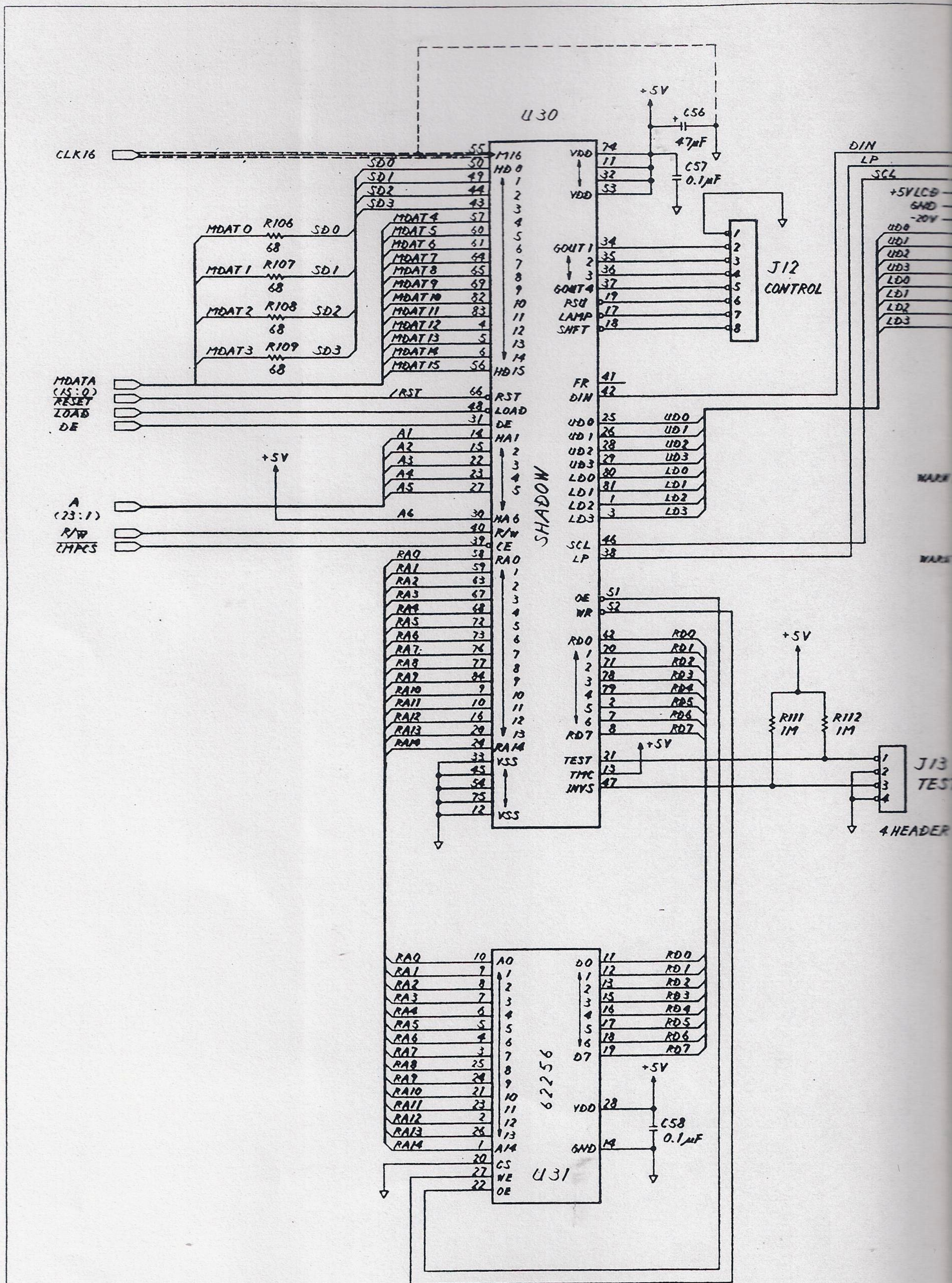
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES DECIMALS ARE TO THREE PLACES FRACTIONS ARE TO SIXTEENTHS HOLE DIA. IS TO BE DRILLED PARTIAL	DRAWN BY <i>N. Thomas</i> 11-2-88 CHECKED 12-8-88 ENGINEER <i>J. Tsuchiya</i> 9-10-88 APPROVED 11-14-89	DATE 11-2-88 DATE 12-8-88 DATE 9-10-88 DATE 11-14-89	ATARI CORPORATION ATARI FILE SCHEMATIC DIAGRAM STACY SIZE D C103419 SCALE NONE SHEET 1 OF 4
	TITLE SCHEMATIC DIAGRAM STACY		
	SIZE D C103419		
	SCALE NONE SHEET 1 OF 4		



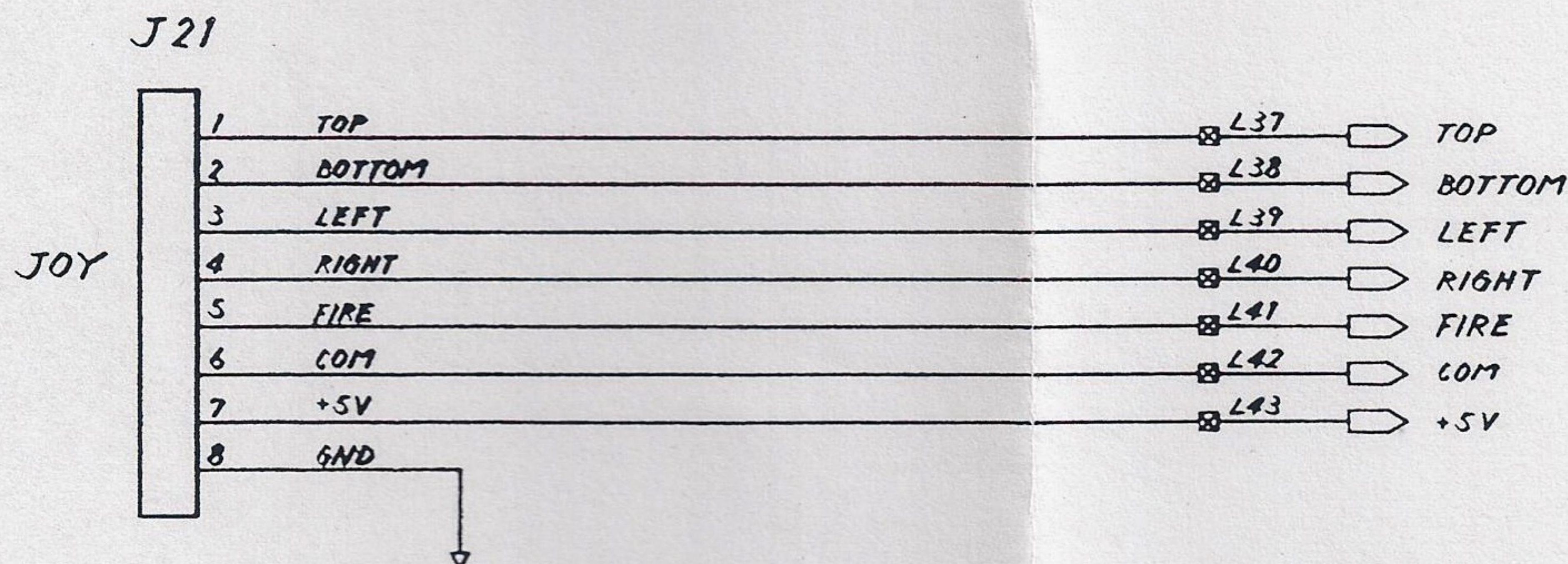
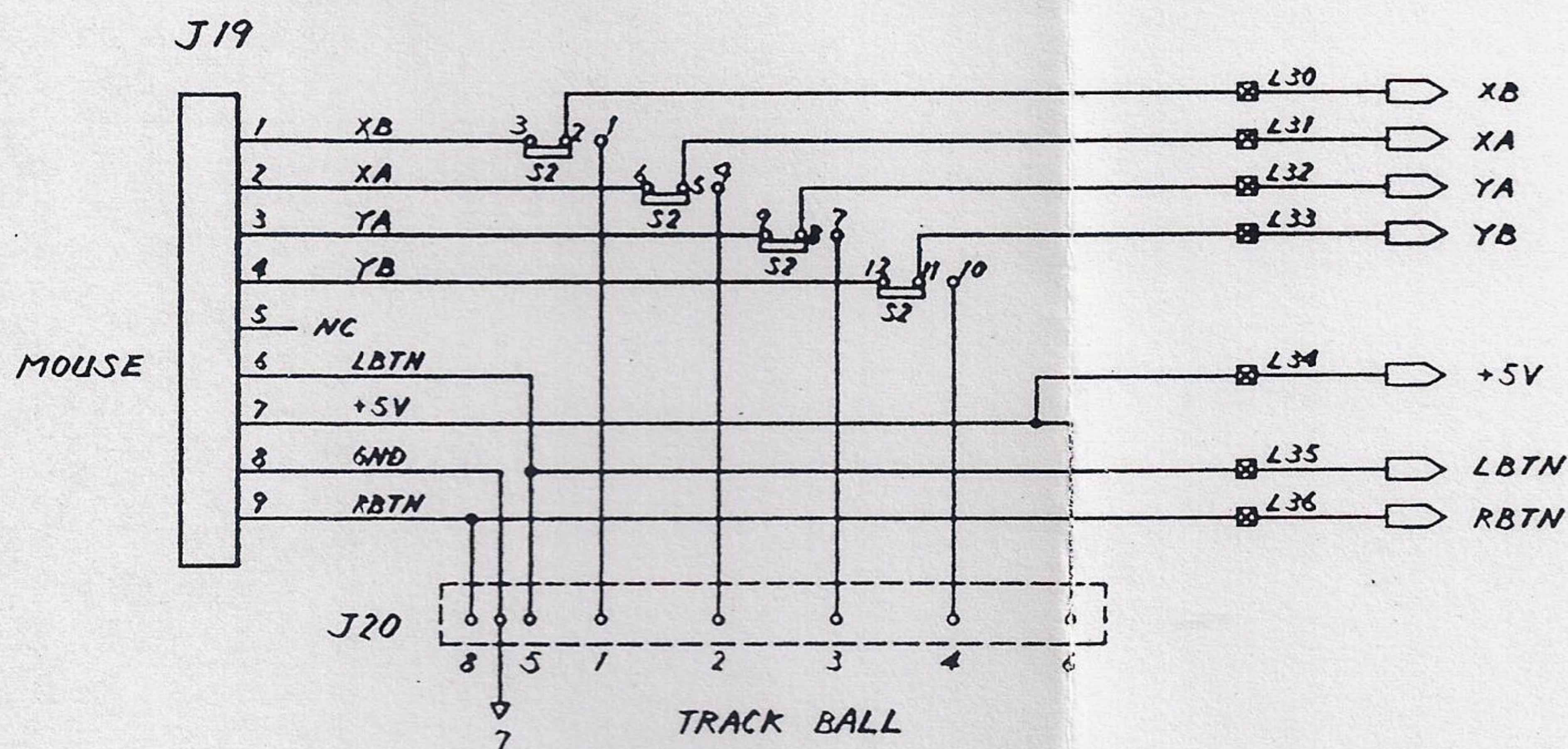
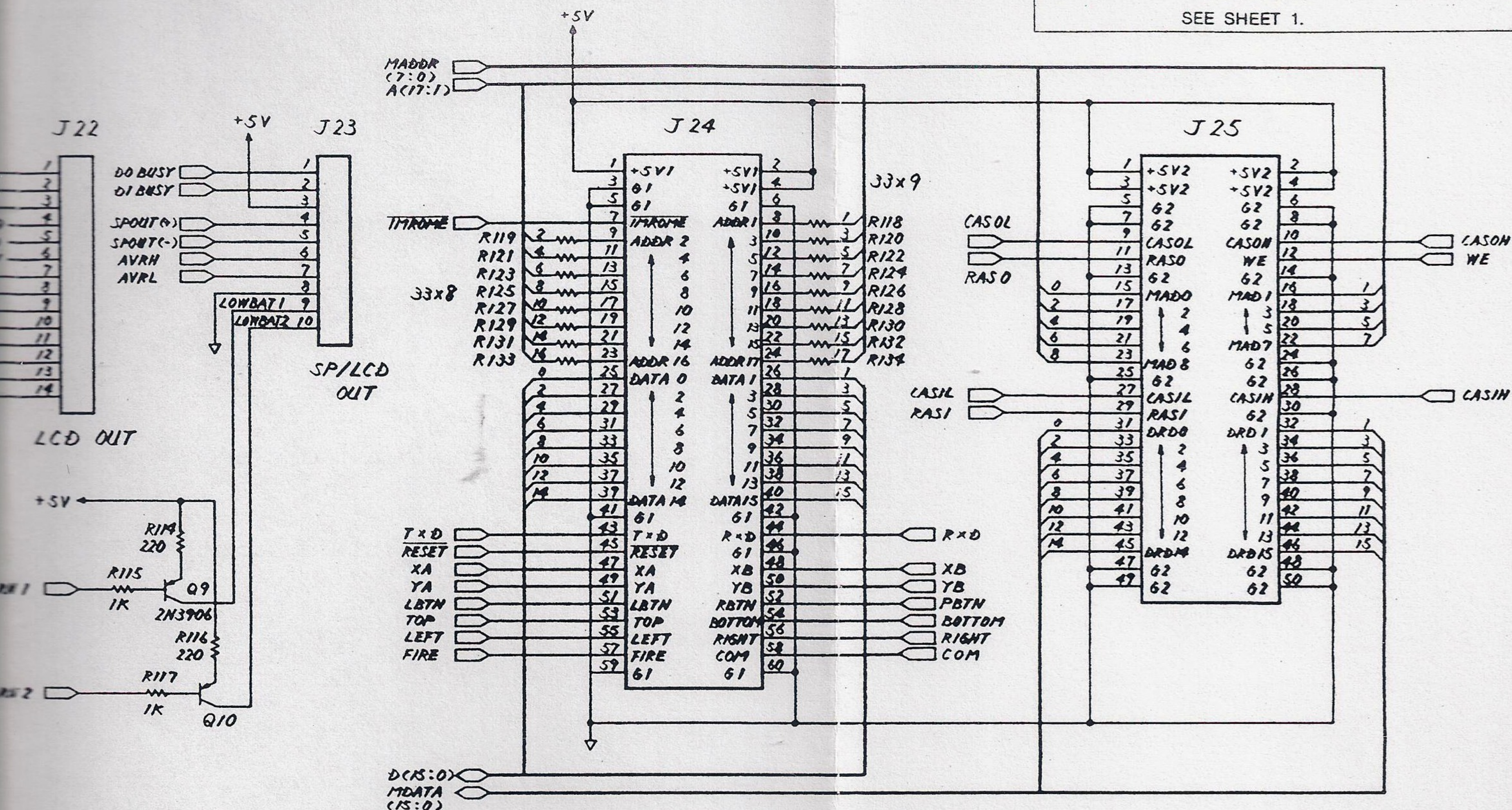




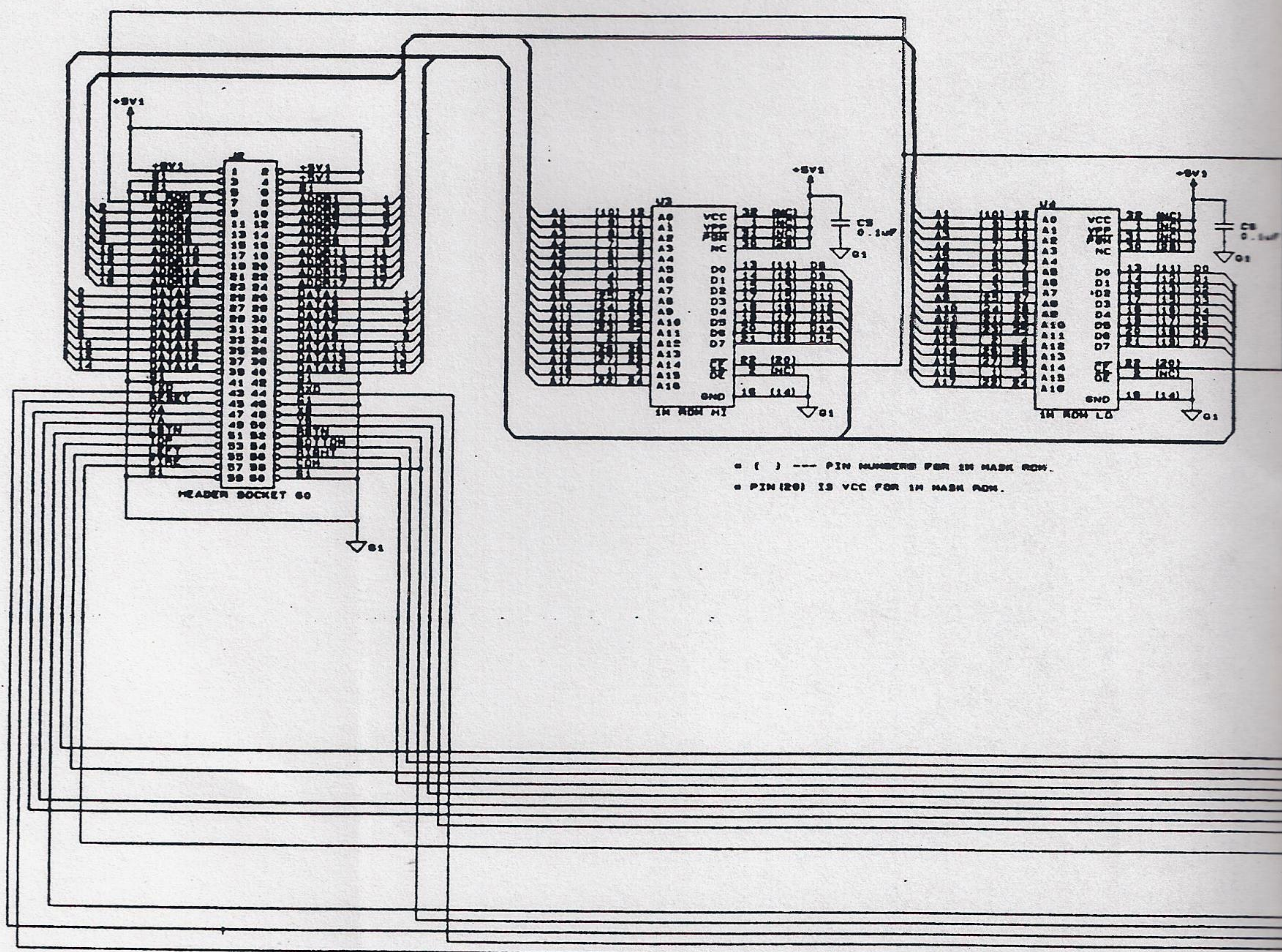
NOTE: - UNLESS OTHERWISE SPECIFIED:



NOTES - UNLESS OTHERWISE SPECIFIED

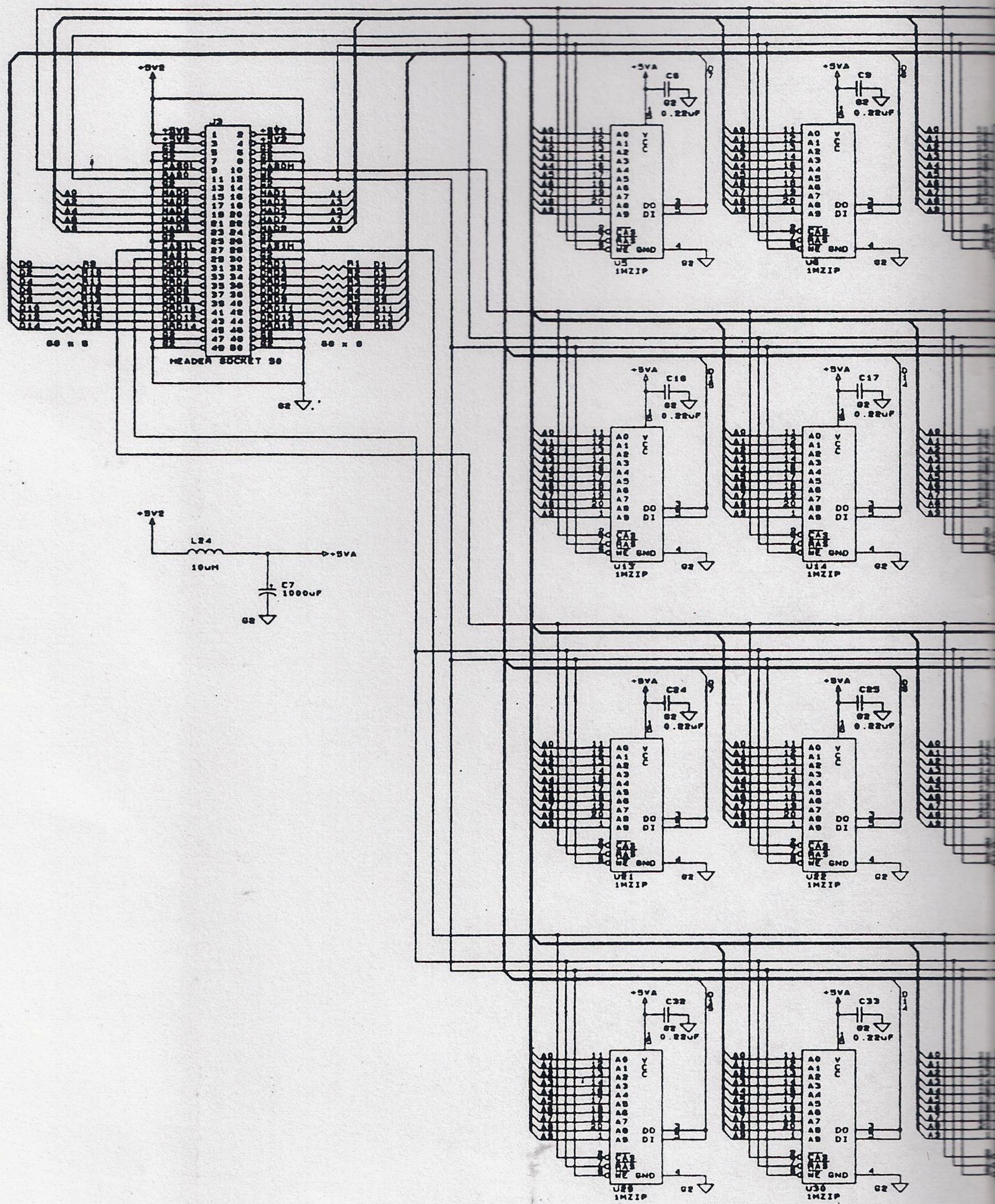


PRESS (OTHERWISE SPECIFY) TOLERANCES IN LINES DRAWN (IN) OR ANIMAL (IN)		DRAWN BY E. KURAMOTO DATE 10-31-88	ATARI CORPORATION SCHEMATIC DIAGRAM STACY SIZE (DRAWING NO) D C103419 SCALE NONE SHEET 4 OF 4
CHECKED BY T. Suchard DATE 12-9-88		APPROVED J. D. MARI DATE 1/1/89	
NEXT ASSY USED ON ORIGINAL MODEL APPLICATION	APPROVED DATE J. D. MARI 1/1/89		



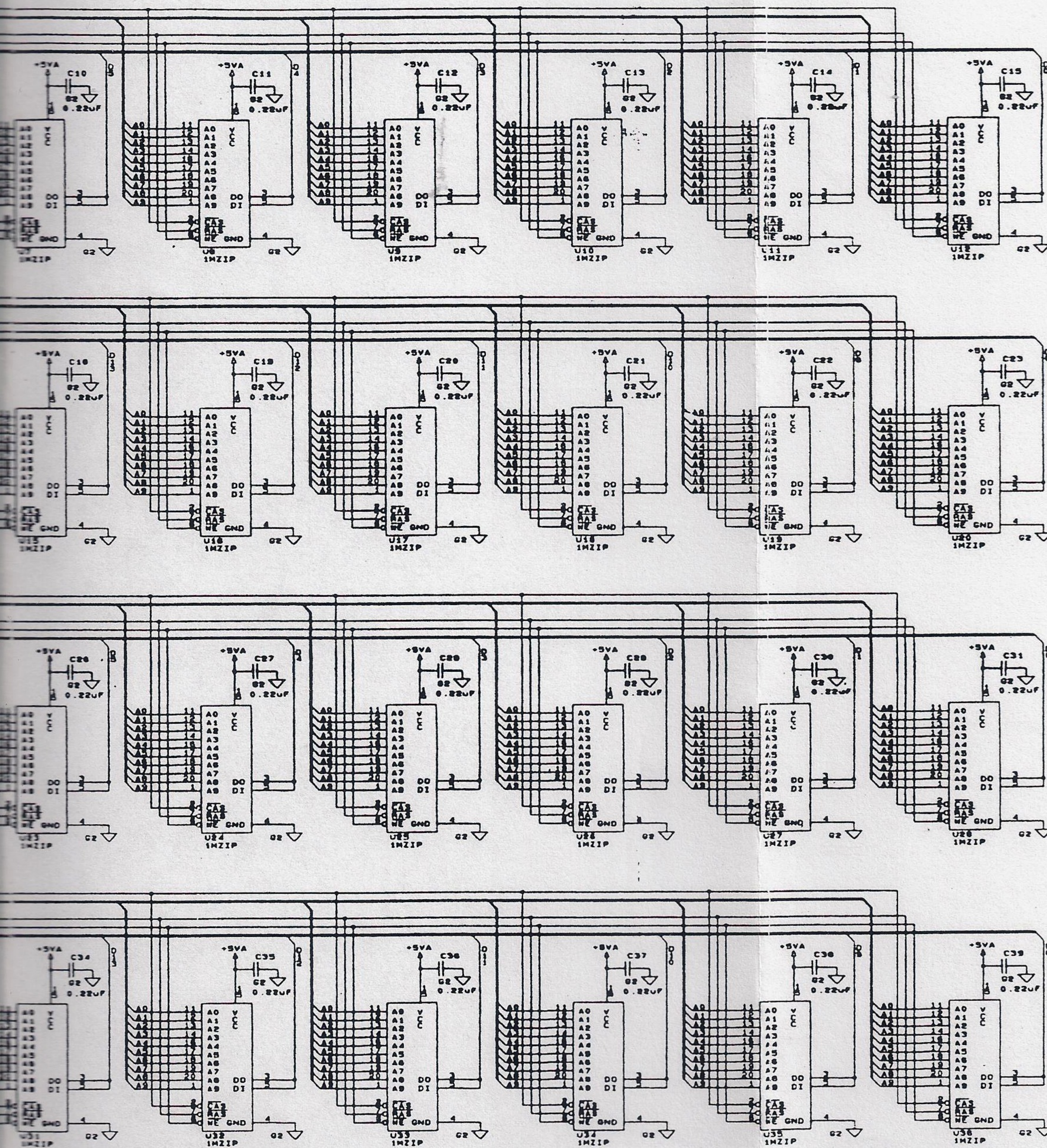
1. RESISTORS ARE MEASURED IN OHMS.

NOTES—UNLESS OTHERWISE SPECIFIED



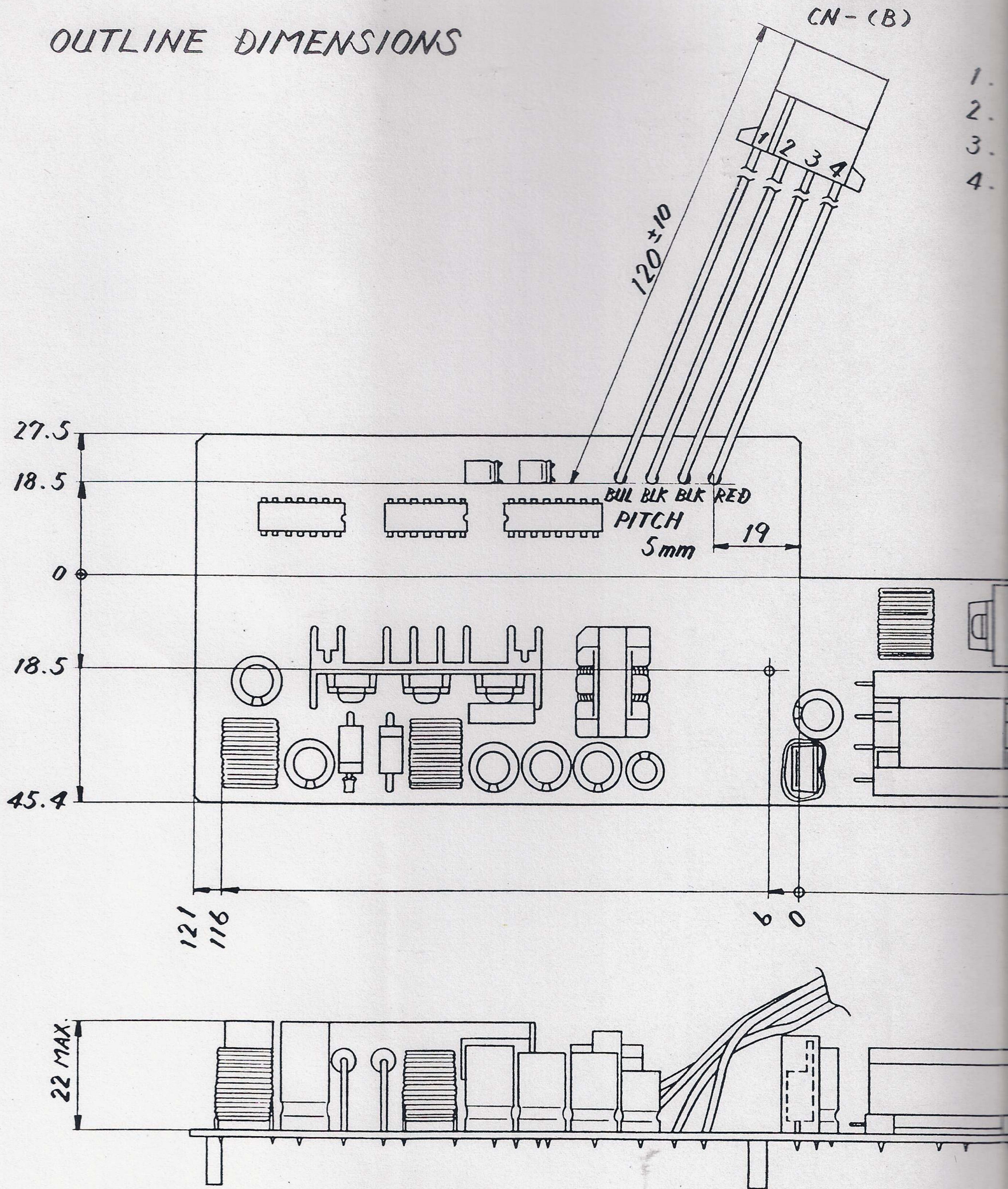
NOTES—UNLESS OTHERWISE SPECIFIED:

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



LESS OTHERWISE SPECIFIED TOLERANCES ON LINEAR DIMENSIONS ON ANGULAR DIMENSIONS UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS IN INCHES DECIMALS FRACTIONS		DRAWN BY 2. Kanda 5-24-89 CHECKED 5-24-89 T. Tsuchida ENGINEER O. Shingawa 5-31-89 APPROVED 6/1/89	DATE DATE DATE DATE DATE
APPLICATION ATARI CORPORATION		ATARI CORPORATION ATARI SCHEMATIC DIAGRAM SUB PCB LST-4 DRAWING NO. C103639 REV D NONE SHEET 2 OF 2	

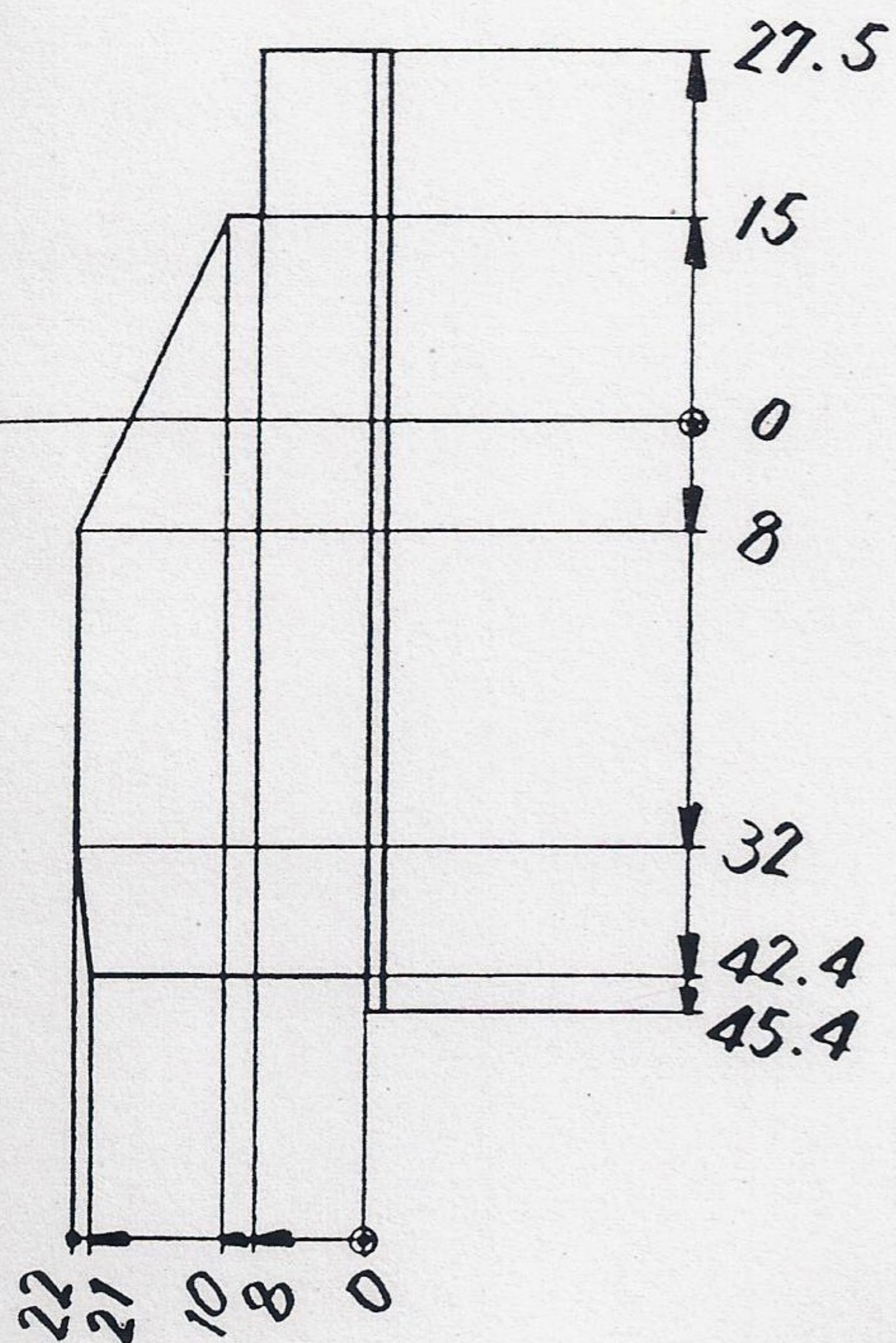
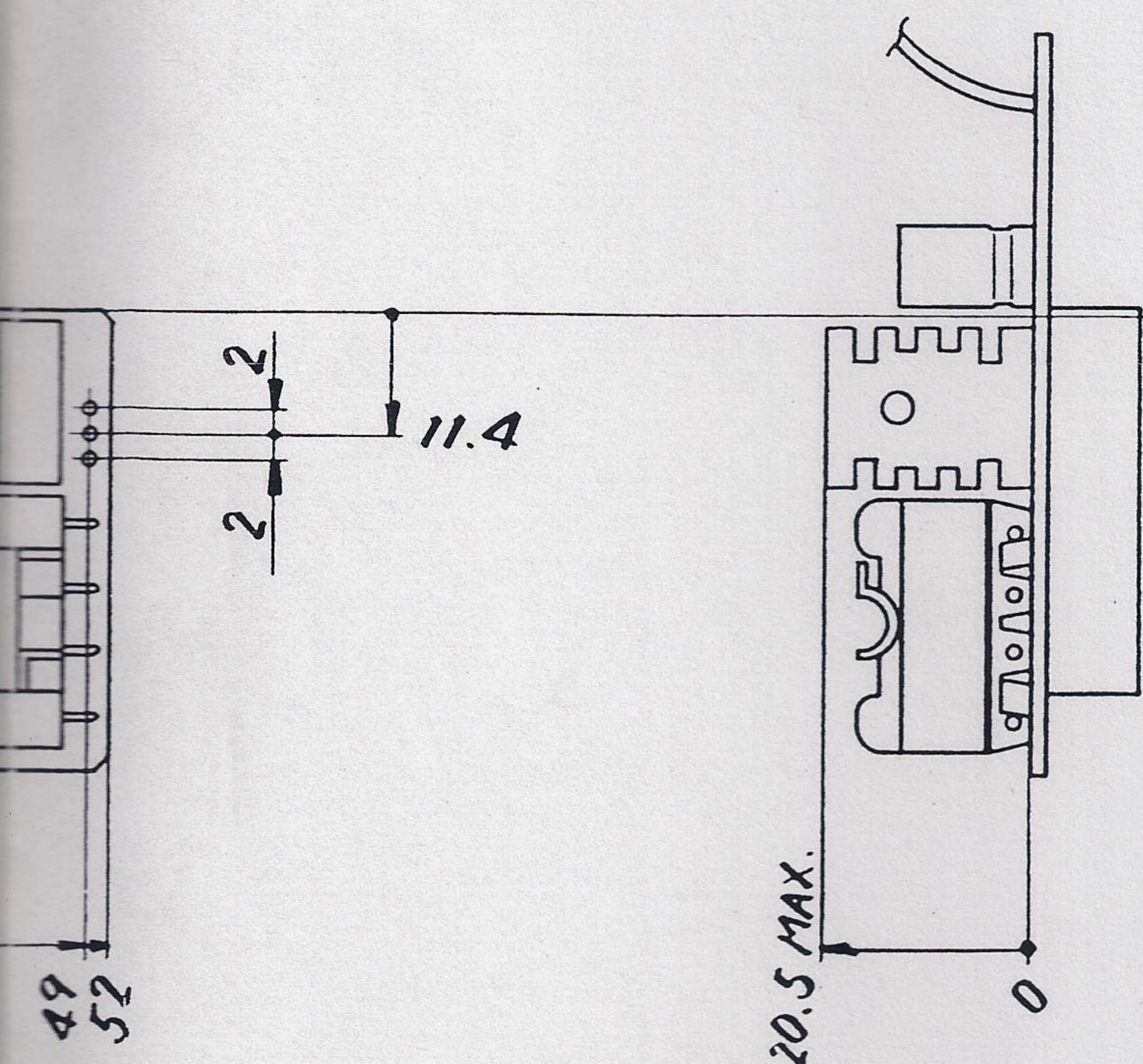
OUTLINE DIMENSIONS




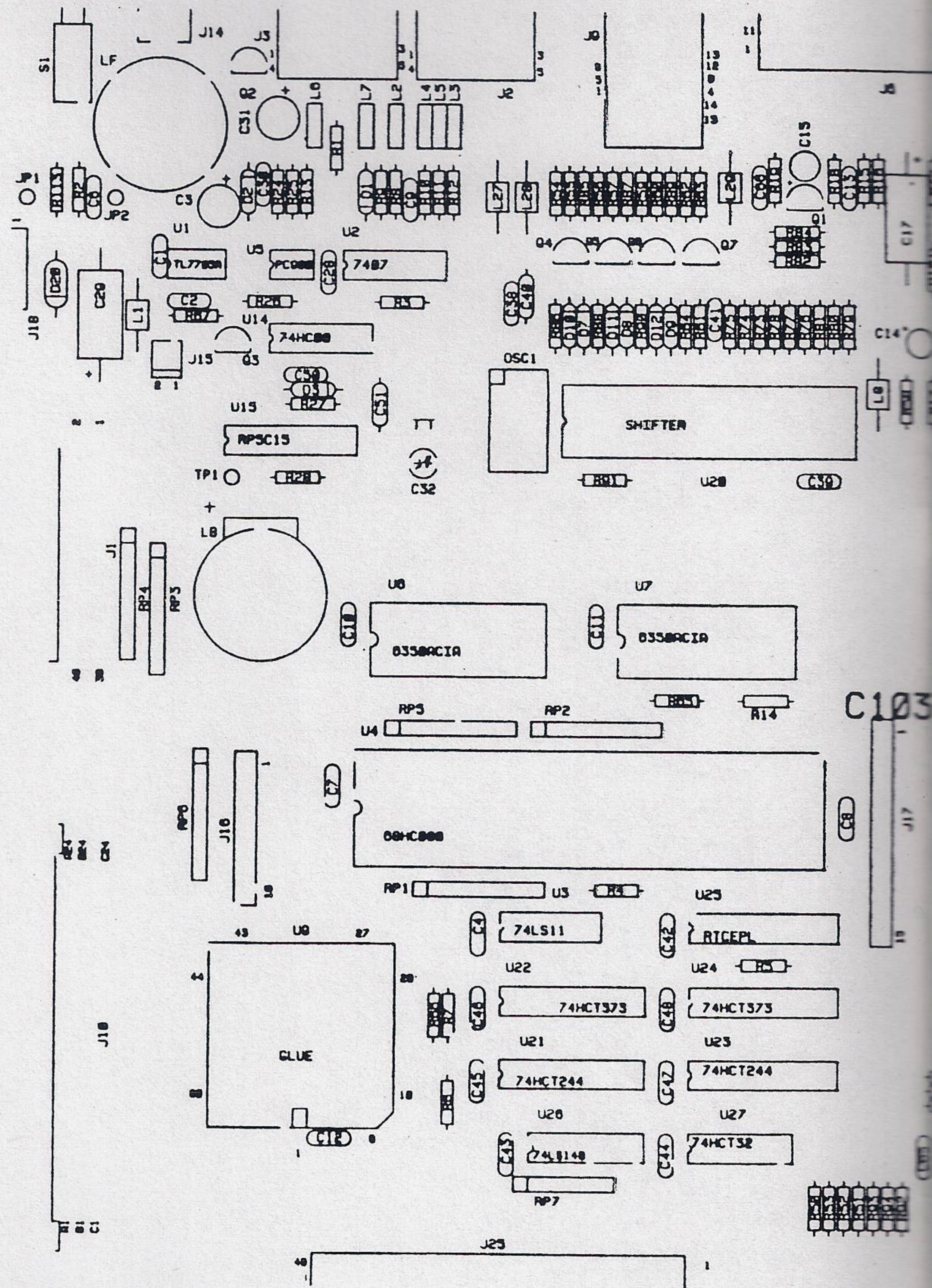
NOTES—UNLESS OTHERWISE SPECIFIED:

NEXT ASSY	ORIG
APPLICATION	

BLU 12V
BLK GND
BLK GND
RED 5V

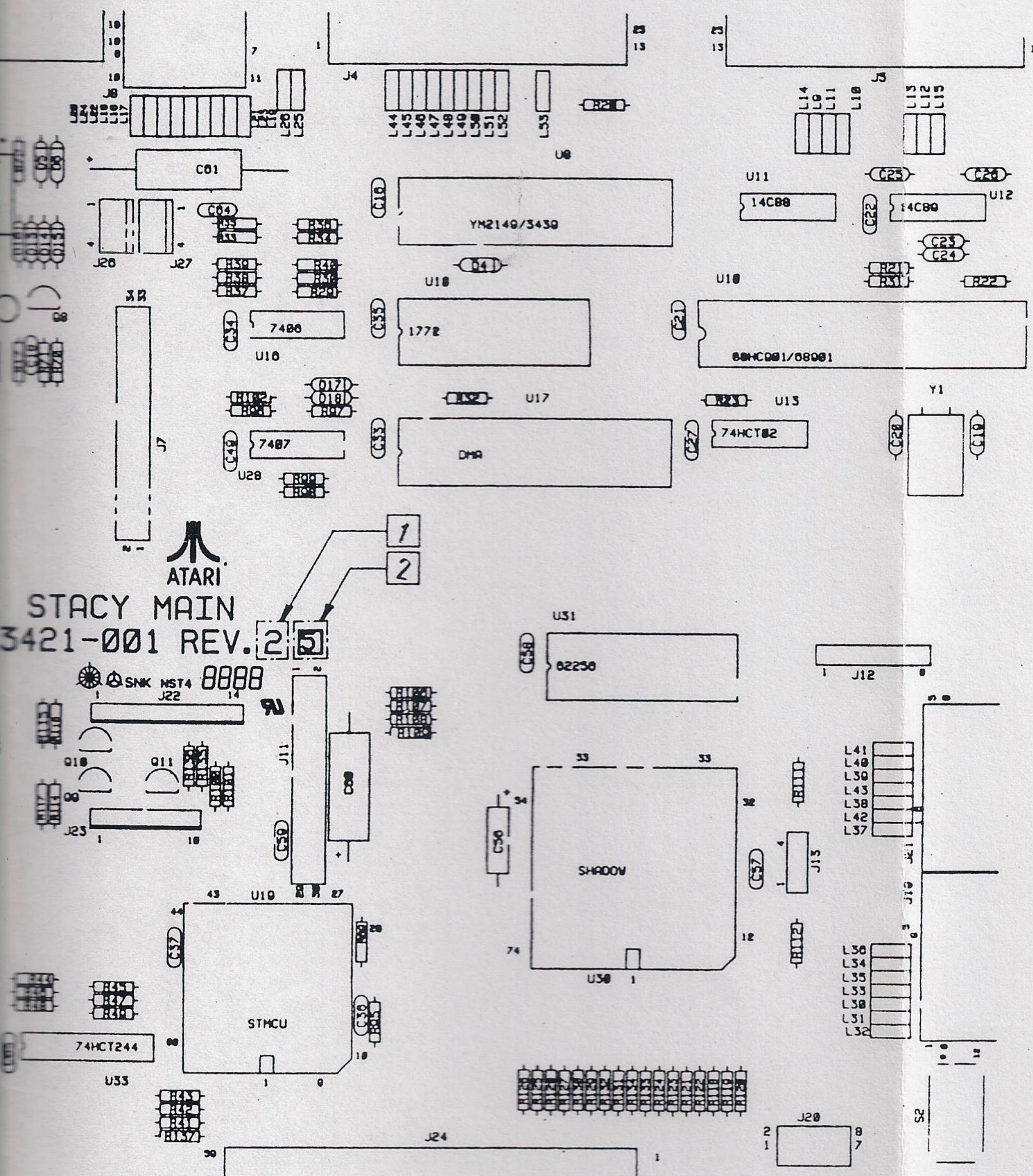


UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY <i>H. Kamba</i> 2-26-90		DATE		 ATARI CORPORATION			
ON LINIER DIMENSIONS		ON ANGULAR DIMENSIONS		CHECKED <i>VN</i> 2/27/90				DATE	
UNDER 30		± 0.1		± 0.5°		ENGINEER		DATE	
30 THRU 300		± 0.2				A. Shinagawa		2-28-90	
OVER 300		± 0.4				APPROVED		DATE	
MATERIAL		FINISH		APPROVED		DATE		TITLE	
USED ON ORIGINAL MODEL		ON		N. OKUBA 2/28/90		SCALE		NONE	
						SIZE		DRAWING NO.	
						B		C101780-xxx	
								REV	
								B	
						SHEET		7 OF 7	



NOTES—UNLESS OTHERWISE SPECIFIED:

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		

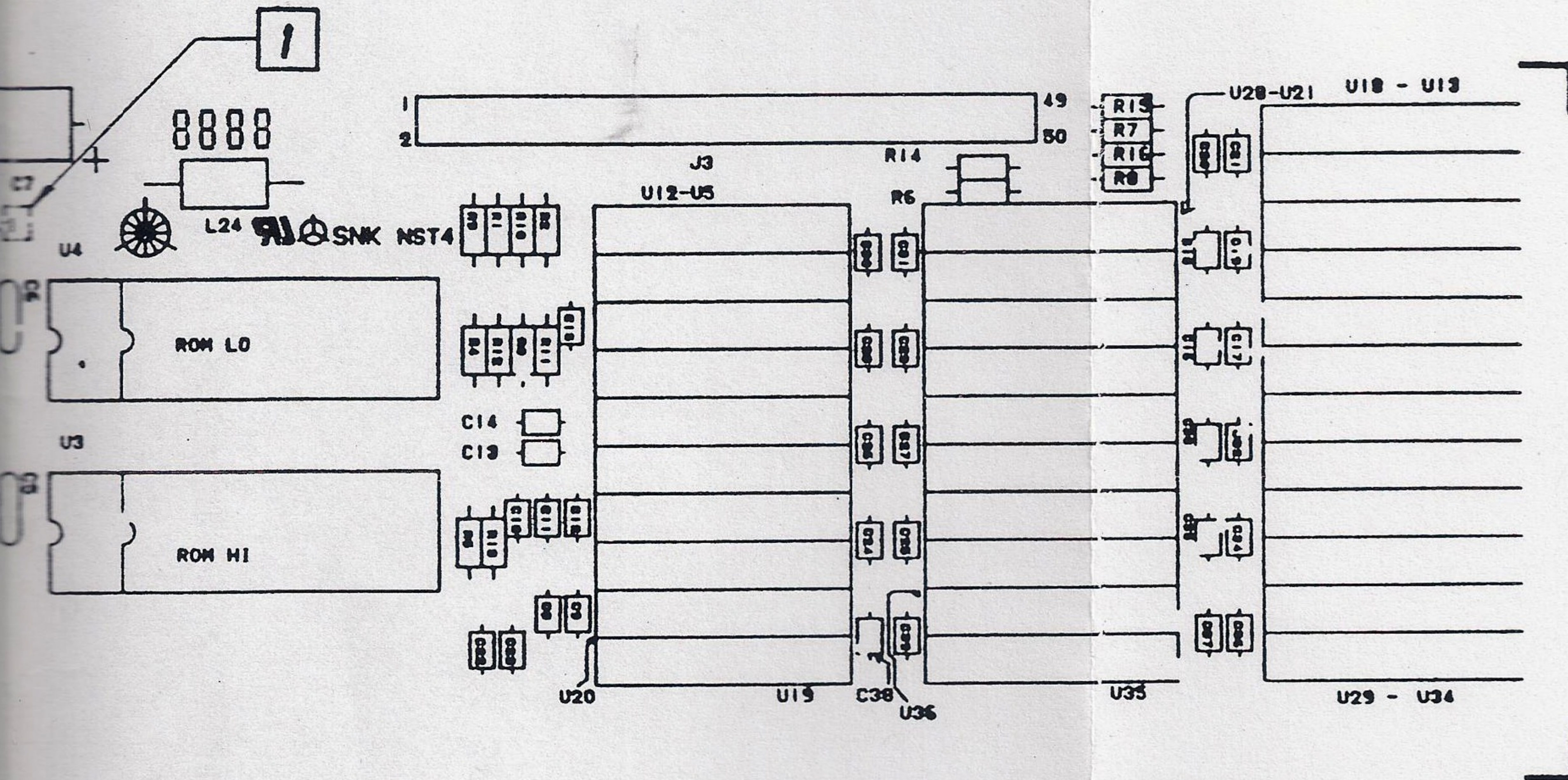


K PRINT


UNLESS OTHERWISE SPECIFIED TOLERANCES ON LINIER DIMENSIONS UNDER 30 ±0.1 30 THRU 300 ±0.2 OVER 300 ±0.4 ON ANGULAR DIMENSIONS ±0.5° MATERIAL FINISH		DRAWN BY	DATE	ATARI CORPORATION TITLE PCB LST SIZE DRAWING NO C 0103421 SCALE NONE SHEET 2 OF 3
		CHECKED	DATE	
NEXT ASSY USED ON ORIGINAL MODEL APPLICATION		ENGINEER	DATE	REV A
		APPROVED	DATE	
		APPROVED	DATE	

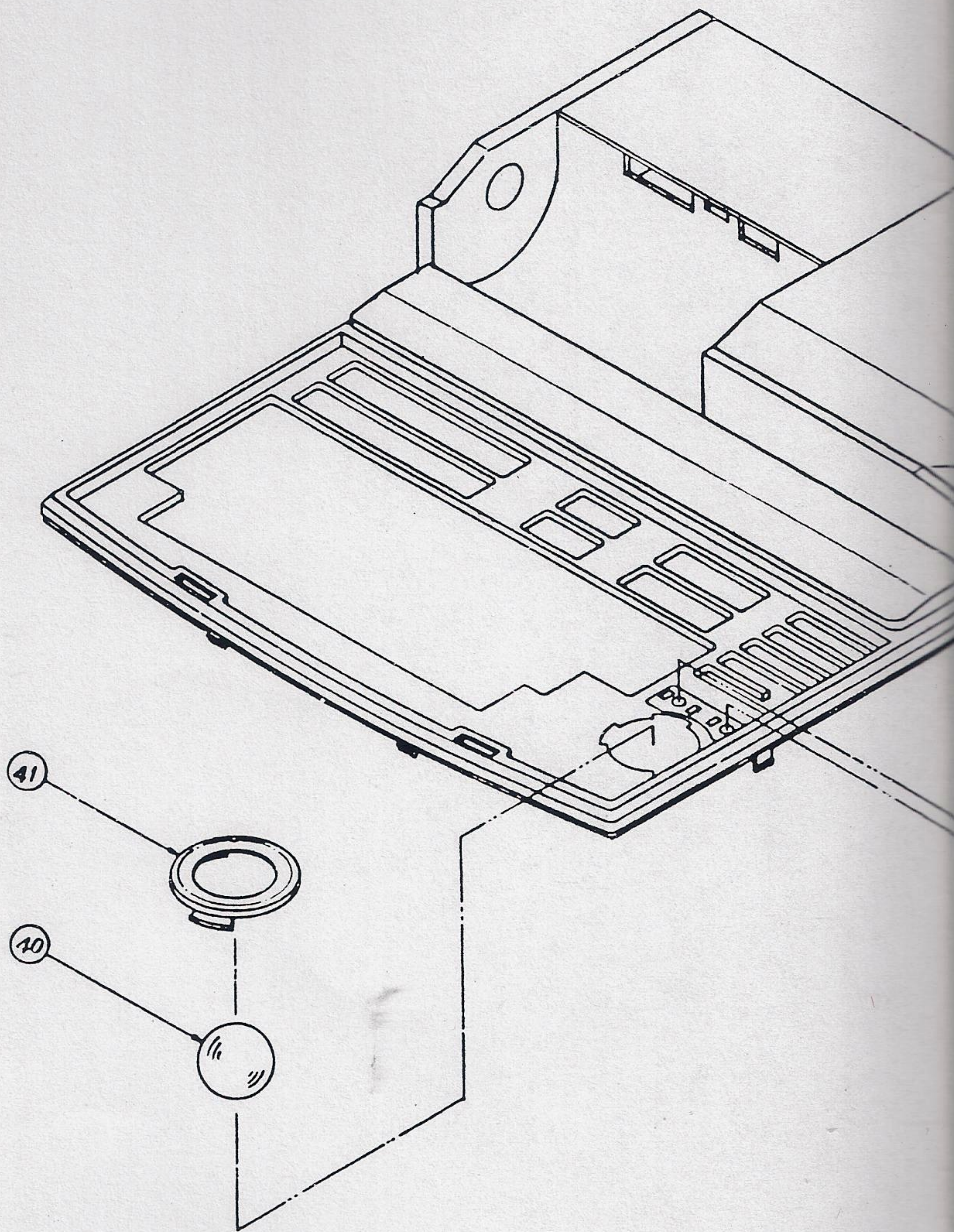
THRU ANGLE SYSTEM DIMENSION

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



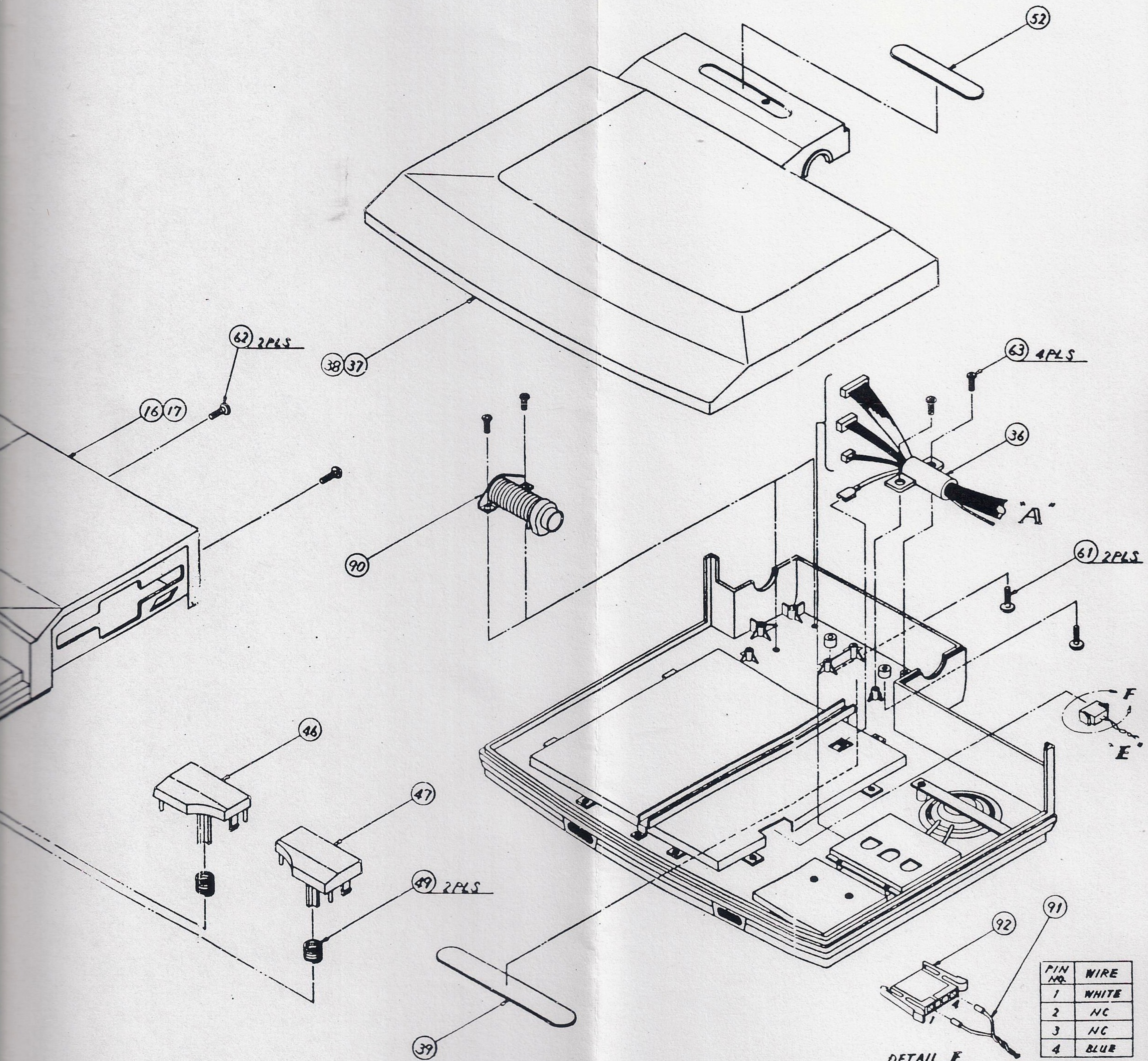
K PRINT

UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY <i>H. Lanza</i> 7-27-89		DATE 7-27-89		 ATARI CORPORATION	
ON LINIER DIMENSIONS	ON ANGULAR DIMENSIONS	CHECKED <i>T. Tschida</i>	DATE 7-27-89	ENGINEER <i>A. Shinagawa</i>	DATE 8-1-89		
UNDER 30	± 0.1	± 0.5°	APPROVED	DATE	SIZE B	DRAWING NO. <i>C103640</i>	REV A
30 THRU 300	± 0.2						
OVER 300	± 0.4						
MATERIAL		APPROVED		DATE	SCALE <i>NONE</i>		
FINISH		APPROVED		DATE	SHEET <i>2</i> OF <i>8</i>		
USED ON ORIGINAL MODEL		APPROVED		DATE	THIRD ANGLE SYSTEM DIMENSION mm		
CATION		APPROVED		DATE			



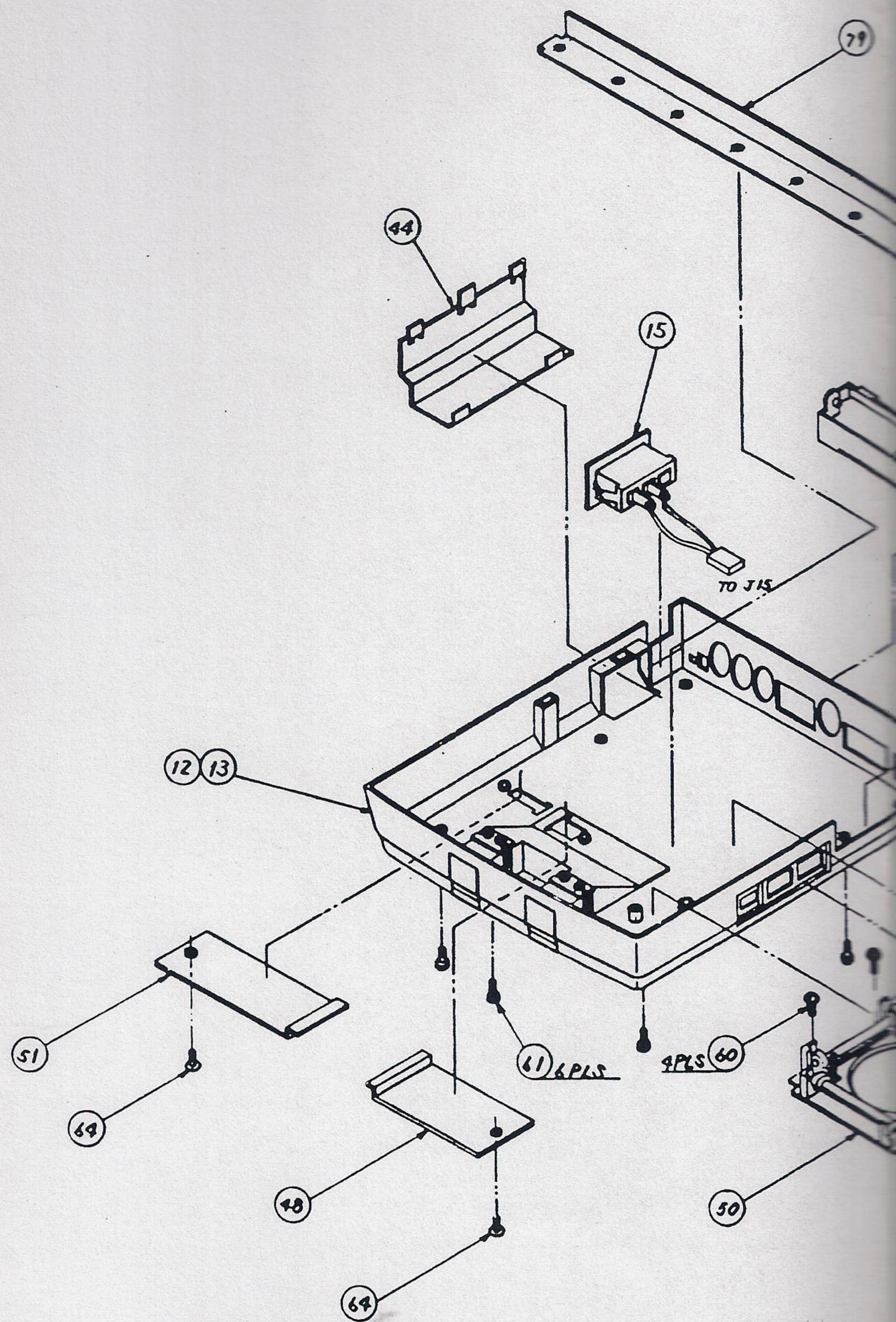
NOTES—UNLESS OTHERWISE SPECIFIED:

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



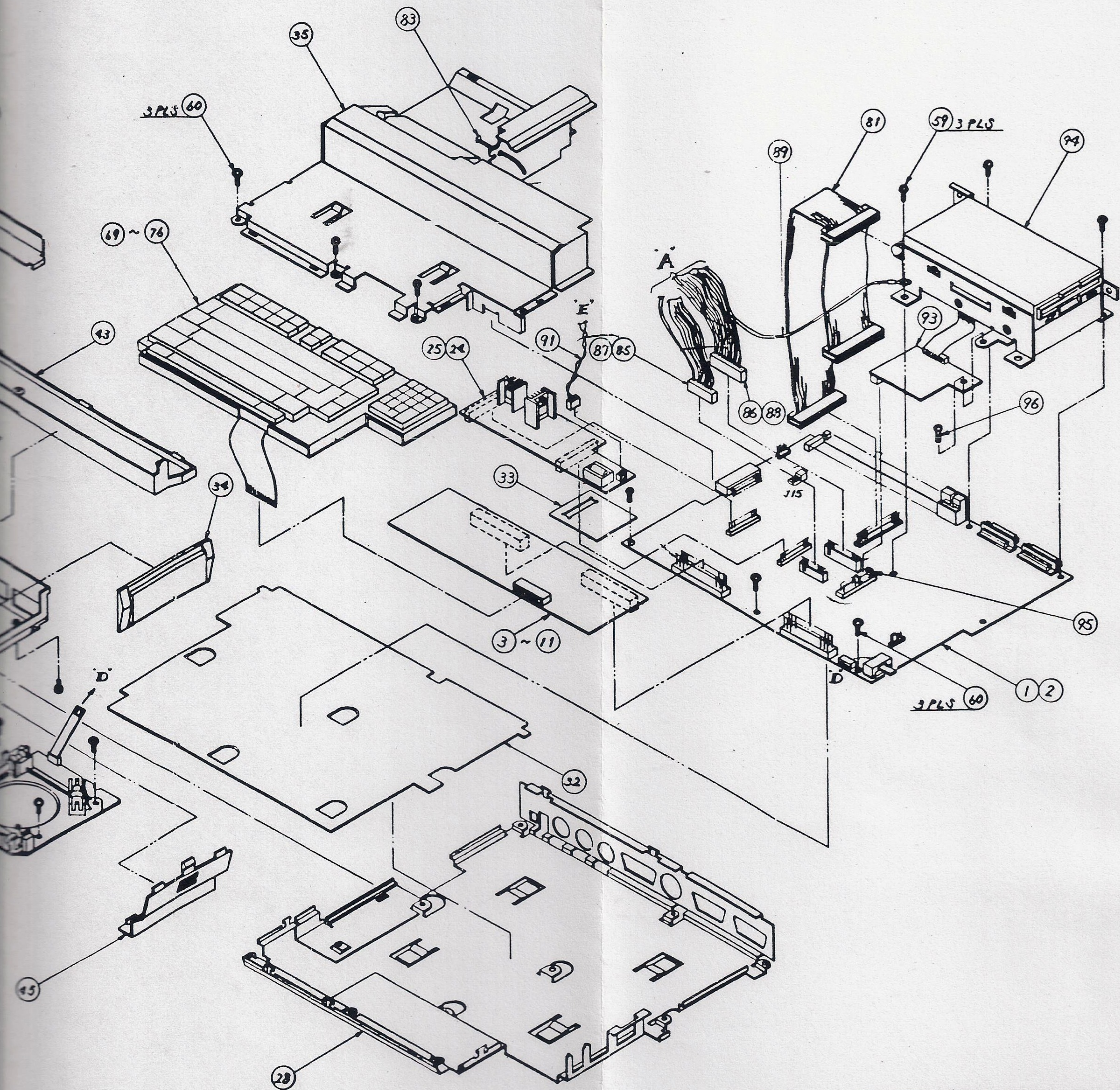
DETAIL E

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE IN INCHES FRACTIONS ARE TO BE USED DECIMALS ARE TO BE USED TOLERANCES ARE: UNDER .010 .005 .010 TO .030 .002 OVER .030 .001		DATE 5-3-89 E. K. KENNEDY CHECKED T. T. T. T. T. ENGINEER	DATE 6-3-89 DATE 6-3-89 DATE 6-3-89 DATE 6-3-89	ATARI CORPORATION ATARI
APPLICATION		TITLE ASSY MAIN LST-4		REV 0
NEXT ASSY		SIZE D		DRAWING NO. CA200532-XXX
USED ON ORIGINAL MODEL		SCALE NONE		SHEET 7 OF 8

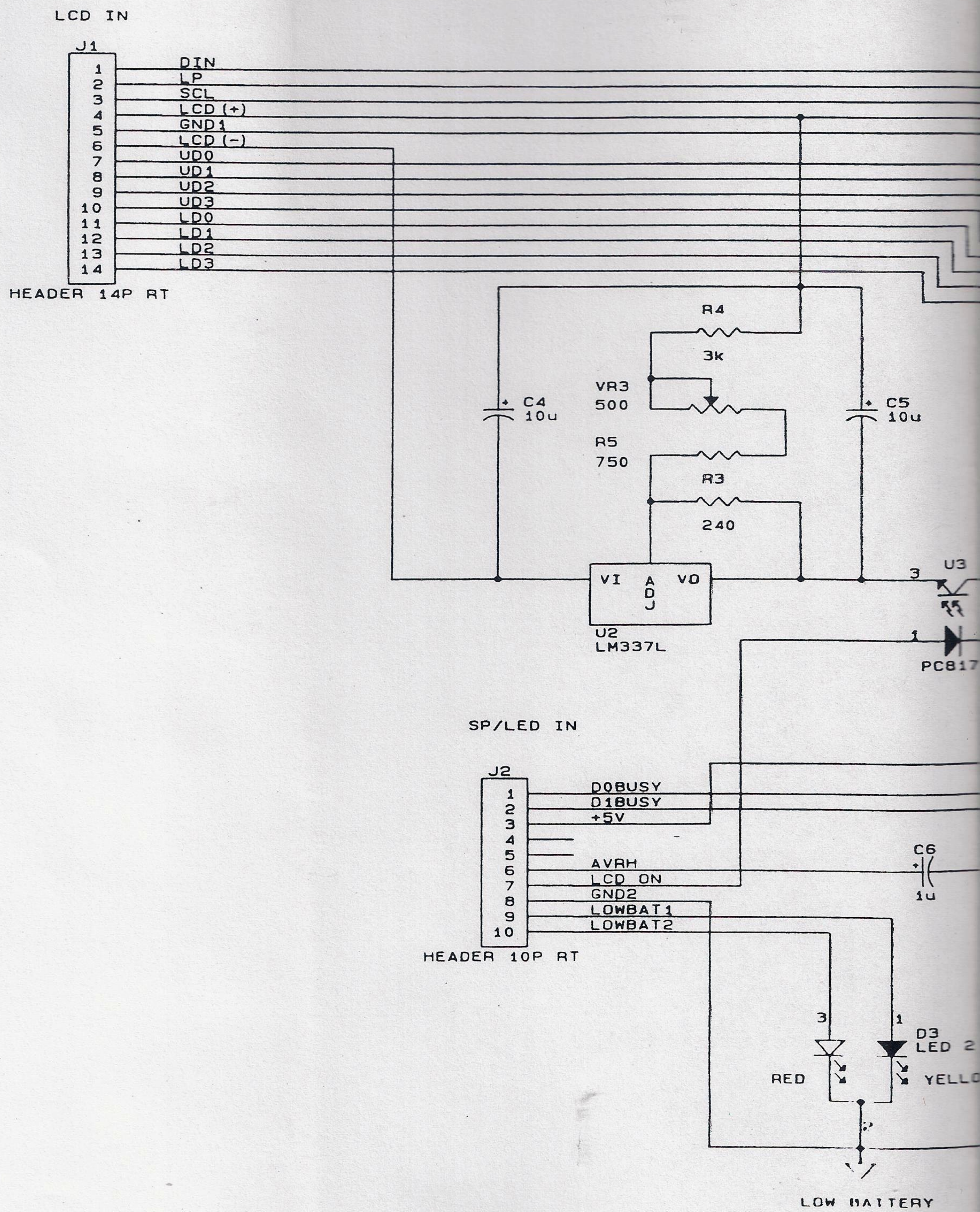


NOTES—UNLESS OTHERWISE SPECIFIED:

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



UNLESS OTHERWISE SPECIFIED OR IN THE DRAWING UNDER 1/8" .01 1/8" TO 1/4" .02 OVER 1/4" .04		CHECKED DATE 6-3-89 APPROVED DATE 4/1/89 APPROVED DATE 1/1/89	CHANGED BY DATE 6-1-89 DATE 6-3-89 DATE 4-1-89 DATE 1-1-89	ATARI CORPORATION ATARI TITLE ASSY MAIN LST-4 SIZE DRAWING NO. D CA200532-XXX SCALE NONE SHEET 8 OF 8
NEXT ASSY USED ON ORIGINAL MODEL APPLICATION		MATERIAL FINISH		



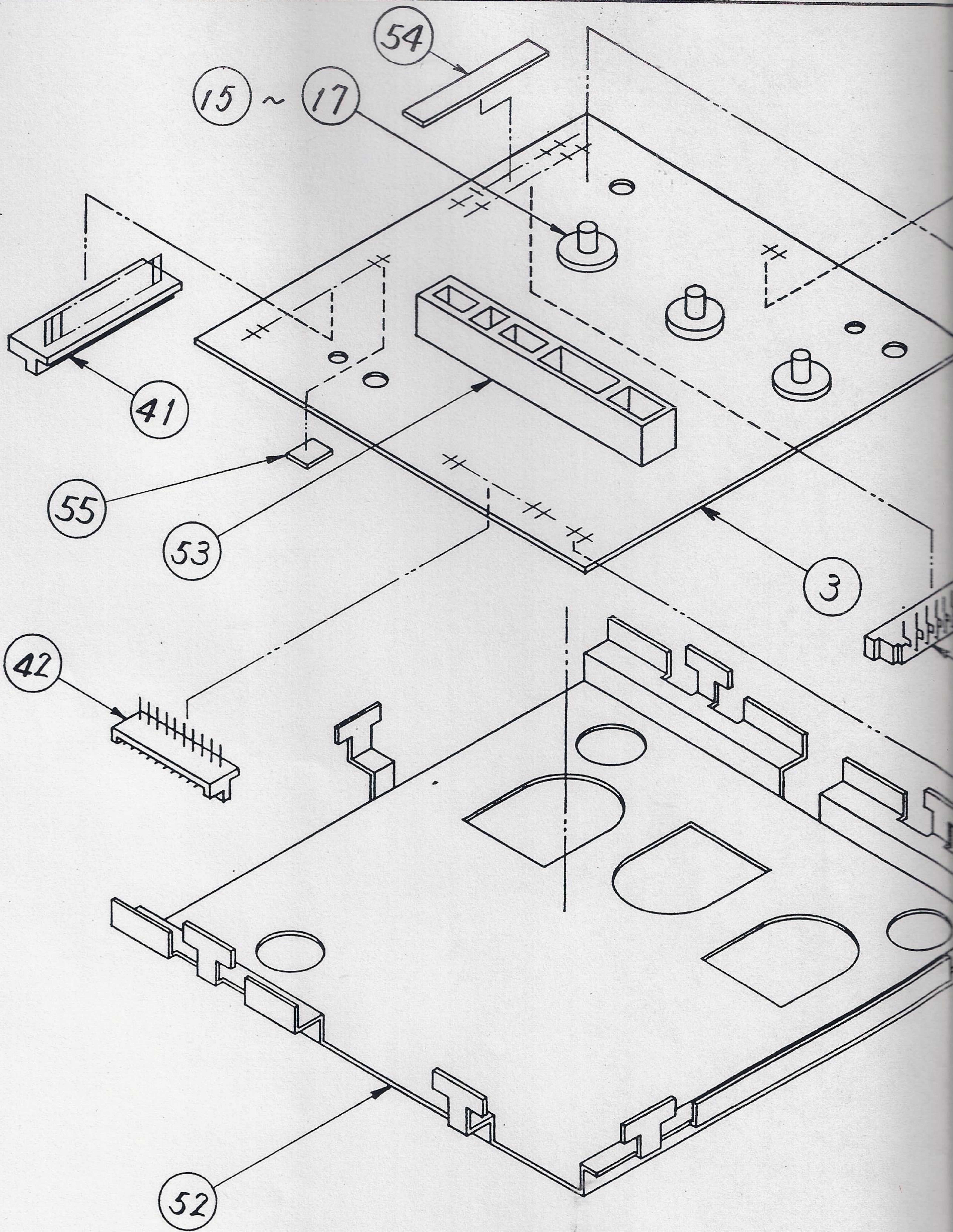
2. CAPACITORS ARE MEASURED IN FARADS.

1. RESISTORS ARE MEASURED IN OHMS.

NOTES-UNLESS OTHERWISE SPECIFIED:

CA2000

NEXT

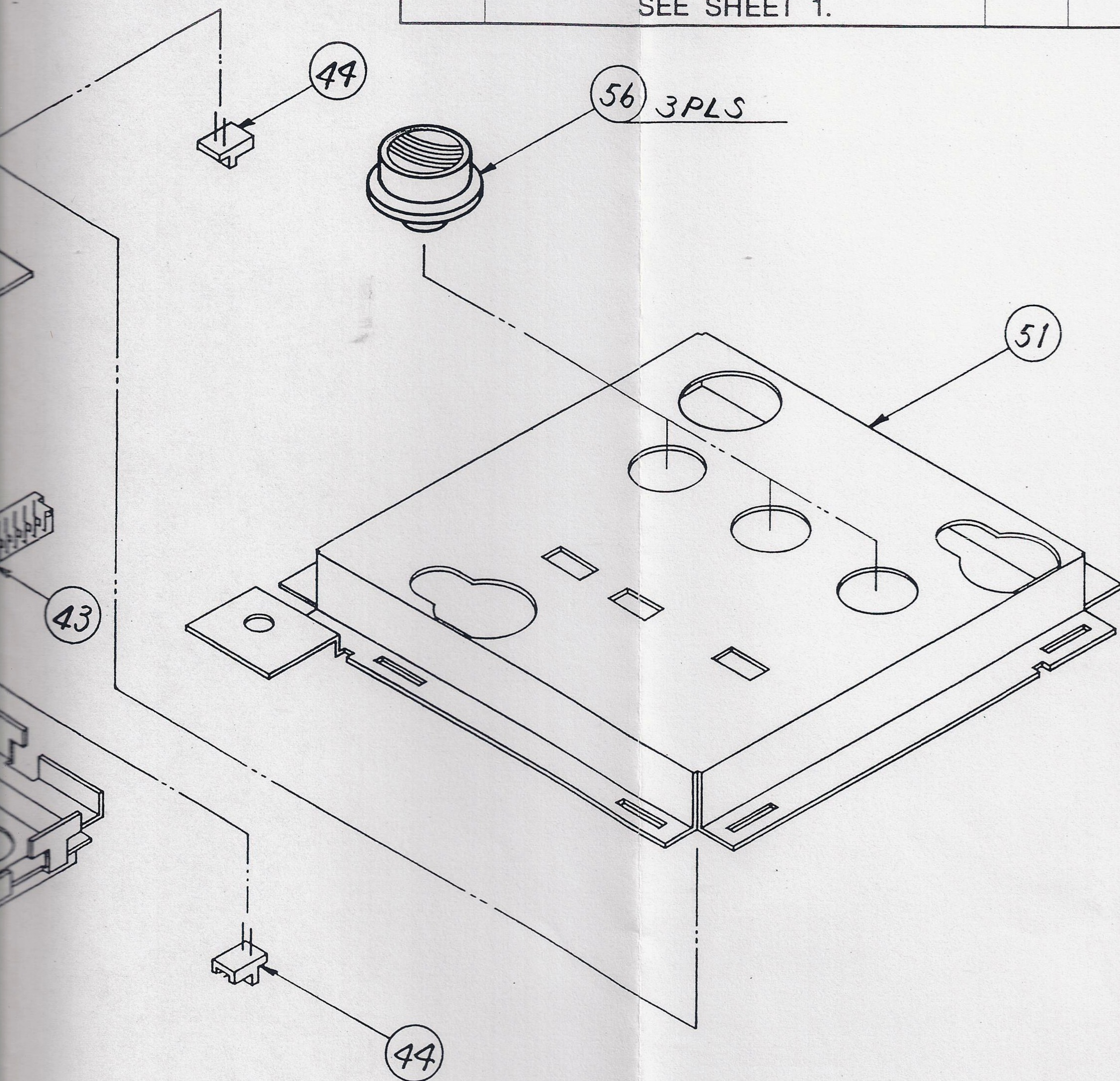


NOTES—UNLESS OTHERWISE SPECIFIED:

NEXT ASSY

APPLI

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



UNLESS OTHERWISE SPECIFIED TOLERANCES		
ON LINER DIMENSIONS	ON ANGULAR DIMENSIONS	
UNDER .30	± 0.1	
.30 THRU .100	± 0.2	± 0.5°
OVER .100	± 0.4	
MATERIAL		
FINISH		

DRAWN BY	DATE
<i>E. Kuramochi</i>	5-8-89
CHECKED	DATE
<i>T. Tsuchida</i>	5-13-89
ENGINEER	DATE
<i>J. Kobayashi</i>	5-18-89
APPROVED	DATE
<i>[Signature]</i>	5/19/89
APPROVED	DATE
<i>A. Shinagawa</i>	5-19-89



ATARI CORPORATION

TITLE

ASSY LED PCB LST

SIZE

B

DRAWING NO.

CA200478-XXX

REV

B

SCALE

NONE

SHEET

5

OF

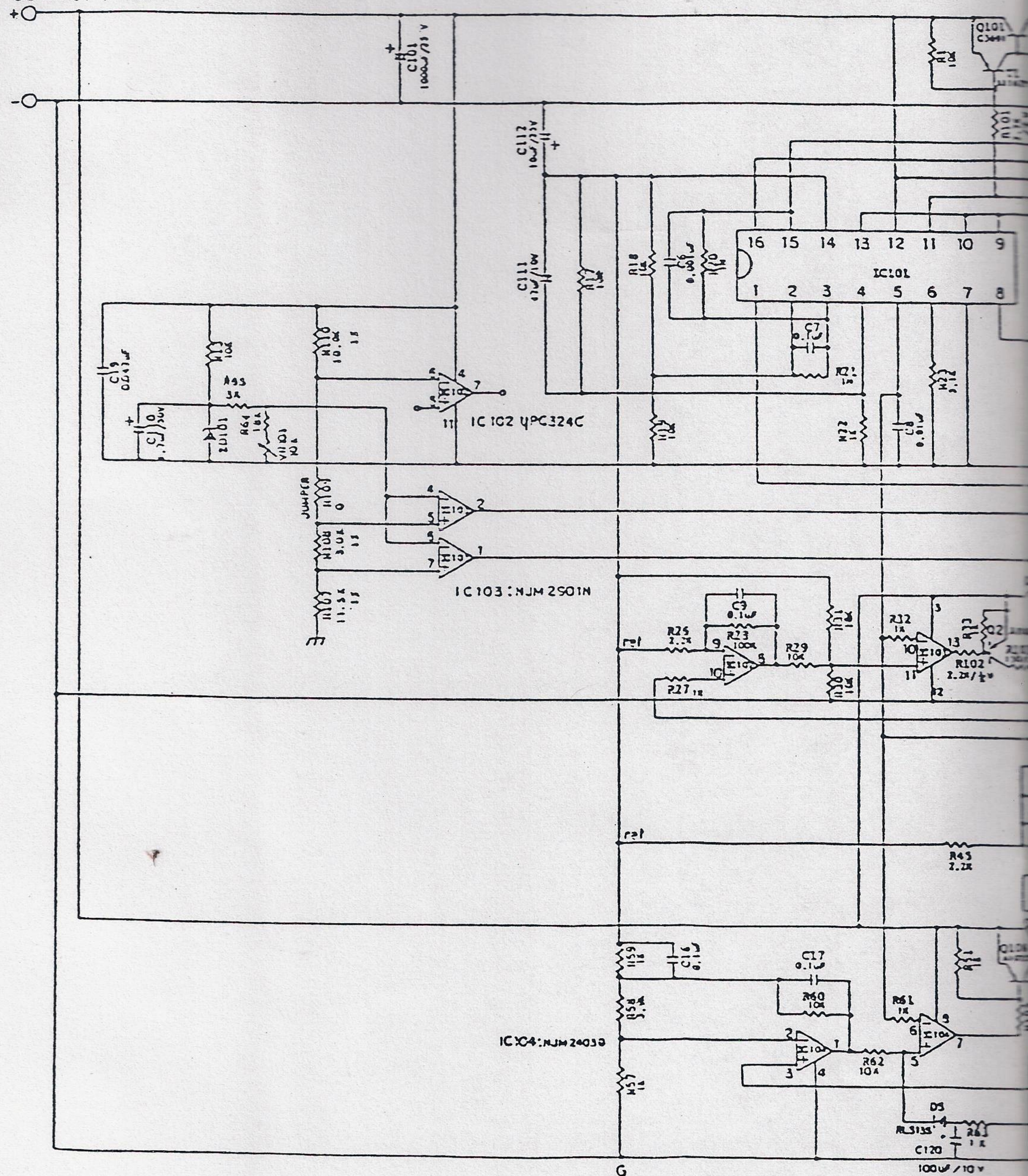
5

USED ON ORIGINAL MODEL

ICATION

SCHEMATIC DIAGRAM

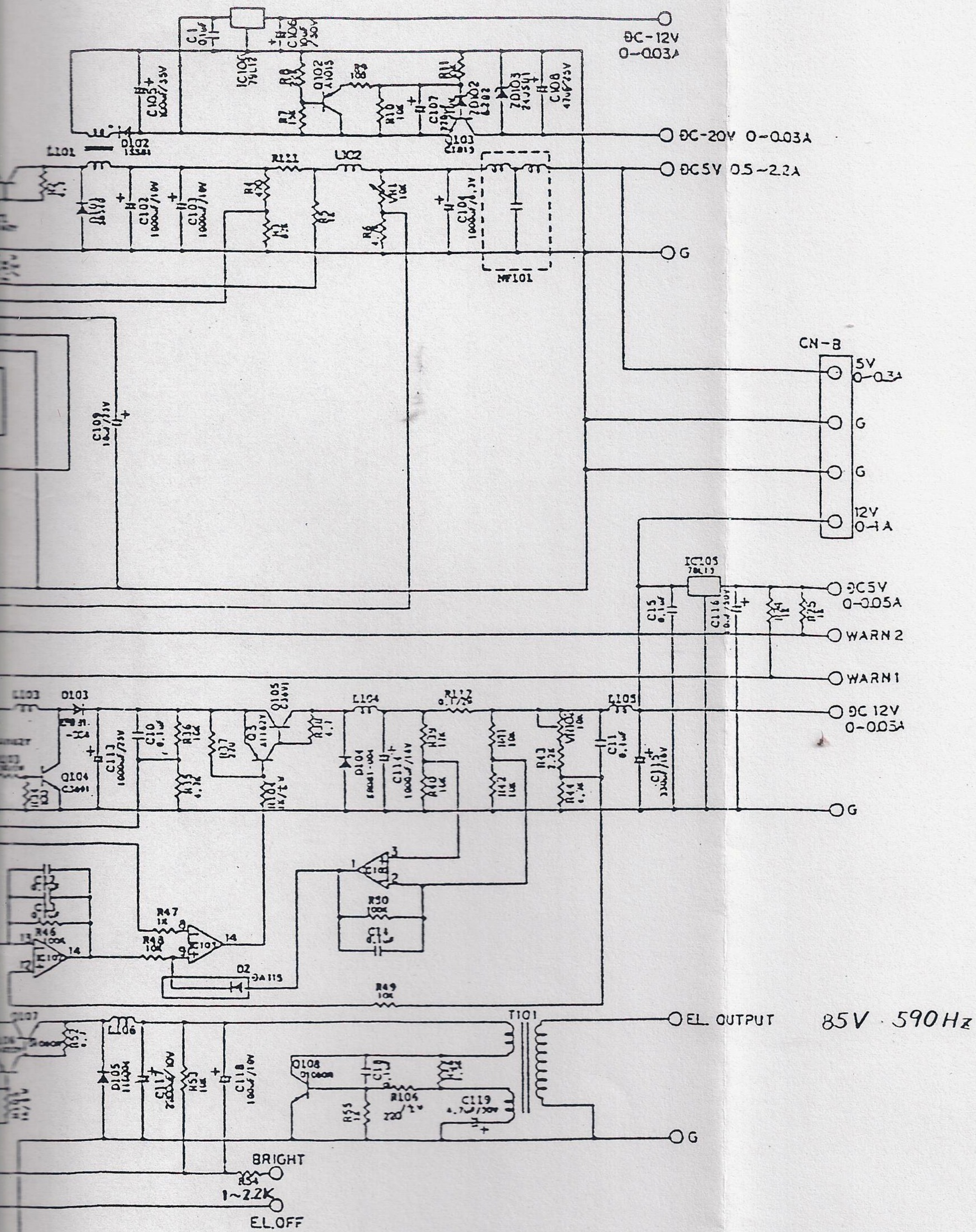
BC INPUT 11-18V




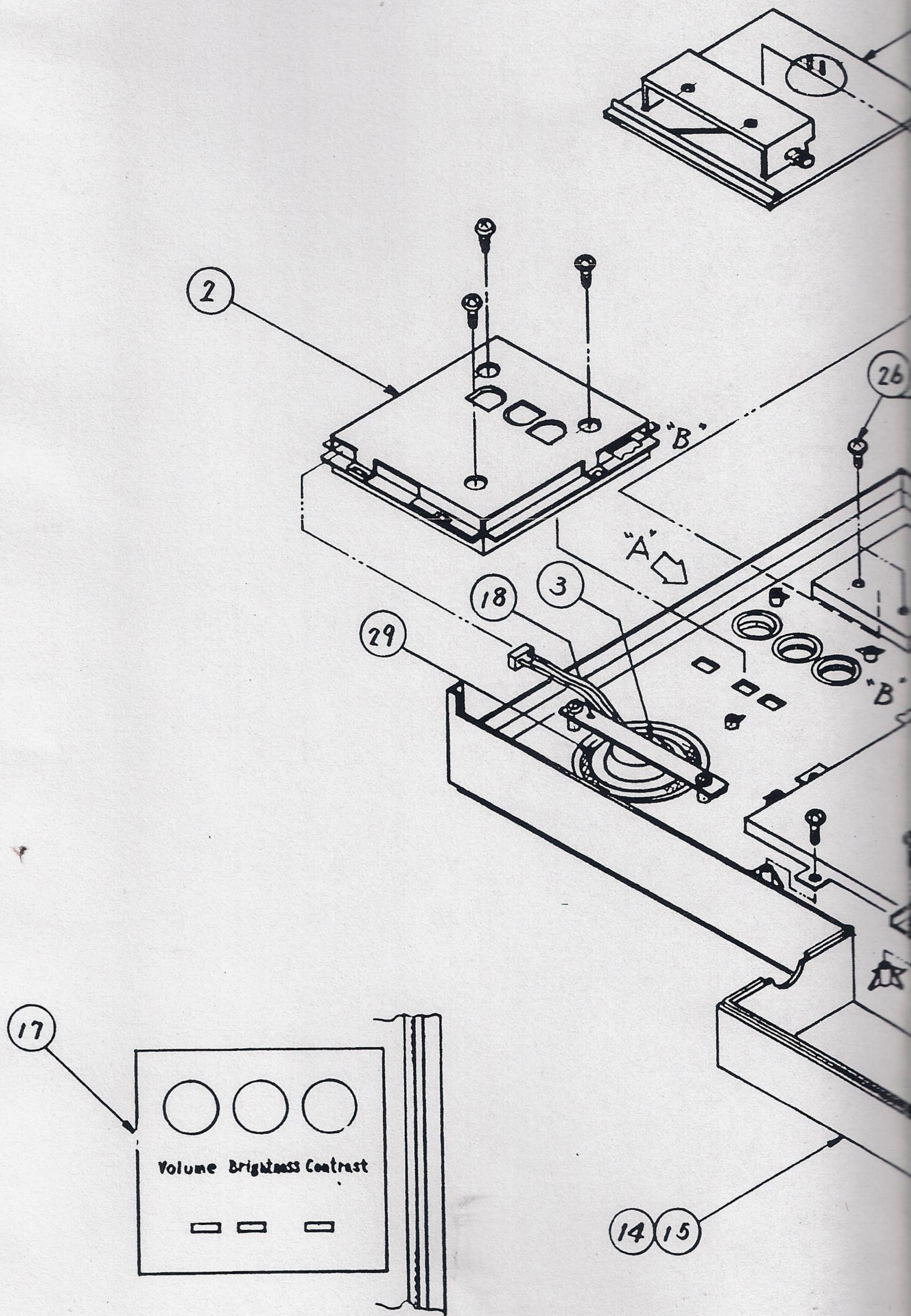
NOTES—UNLESS OTHERWISE SPECIFIED:

NEXT ASSY

APPLICA

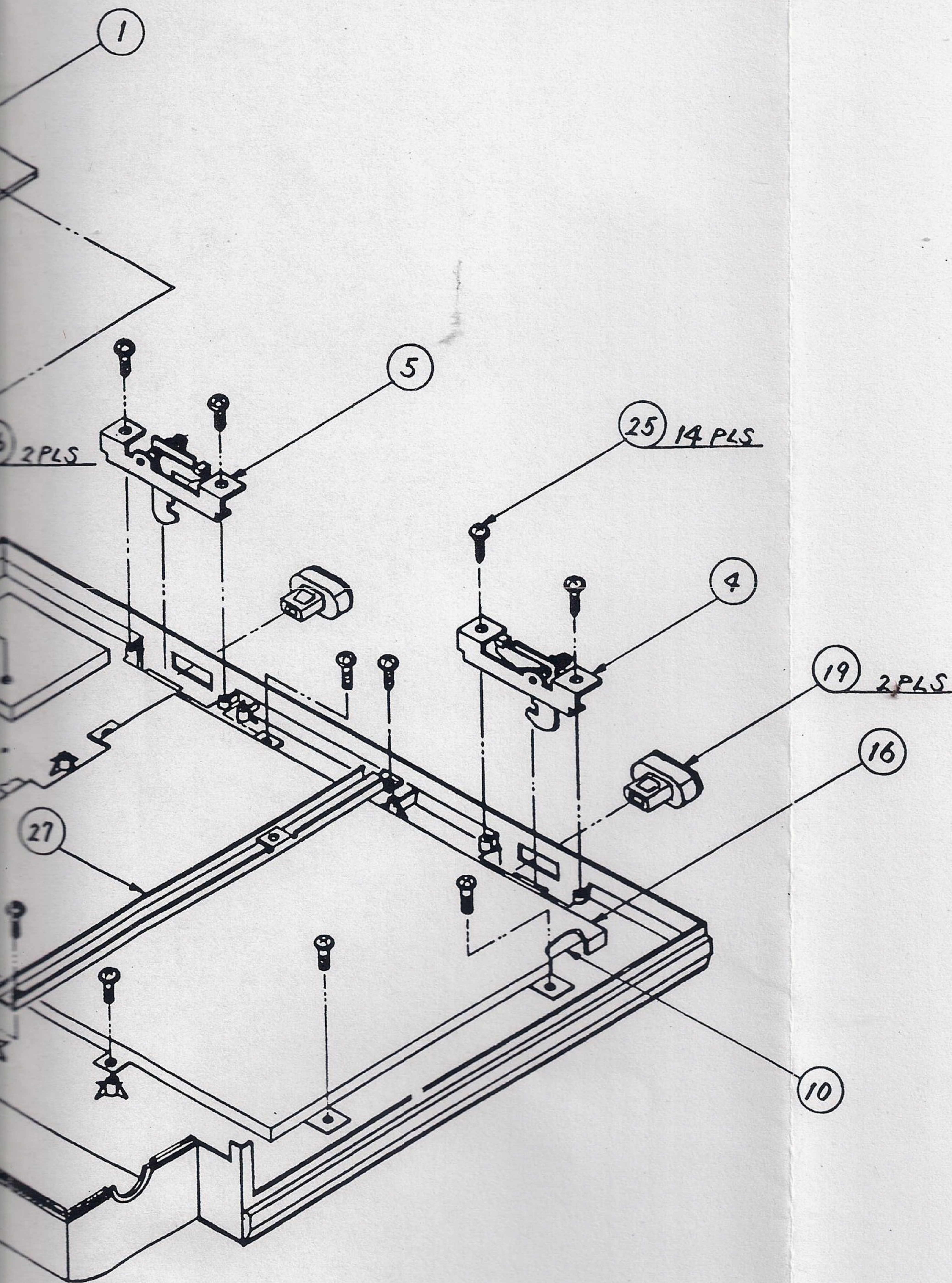


UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY <i>K. Kamba</i> 2-28-90		DATE 2-28-90		 ATARI CORPORATION	
ON LINIER DIMENSIONS	ON ANGULAR DIMENSIONS	CHECKED <i>Y/N</i> 2/28/90	DATE 2-28-90	ENGINEER <i>A. Shinagawa</i>	DATE 2-28-90		
UNDER 30	± 0.1	± 0.5°	APPROVED	DATE	SIZE B	DRAWING NO. C101780-xxx	REV B
30 THRU 300	± 0.2		APPROVED	DATE			
OVER 300	± 0.4		APPROVED	DATE			
MATERIAL		APPROVED		DATE	SCALE NONE		
FINISH		APPROVED		DATE	SHEET 6 OF 7		
USED ON ORIGINAL MODEL		APPROVED		DATE	THIRD ANGLE SYSTEM DIMENSION - mm		



VIEW A

NOTES—UNLESS OTHERWISE SPECIFIED:



REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		

UNLESS OTHERWISE SPECIFIED TOLERANCES ON LINEAR DIMENSIONS UNDER 30 ± 30 THRU 300 ± OVER 300 ± ON ANGULAR DIMENSIONS ± MATERIAL FINISH		DRAWN BY <i>K. T. ...</i> CHECKED <i>T. T. ...</i> ENGINEER <i>[Signature]</i> APPROVED <i>[Signature]</i> APPROVED <i>G. Shingano</i>	DATE 5-8-89 DATE 5-13-89 DATE 5-16-89 DATE 5/18/89 DATE 5-19-89	ATARI CORPORATION TITLE ASSY LCD PANEL LST SIZE C SCALE NONE	DRAWING NO. CA200475-XXX REV C SHEET 4 OF 4
---	--	---	--	--	---

THIRD ANGLE SYSTEM DIMENSION